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BRIEFINGS
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Security Research on Mercedes-Benz: From Hardware to Car Control

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Security Research on Mercedes-Benz

Defending a Luxury Fleet

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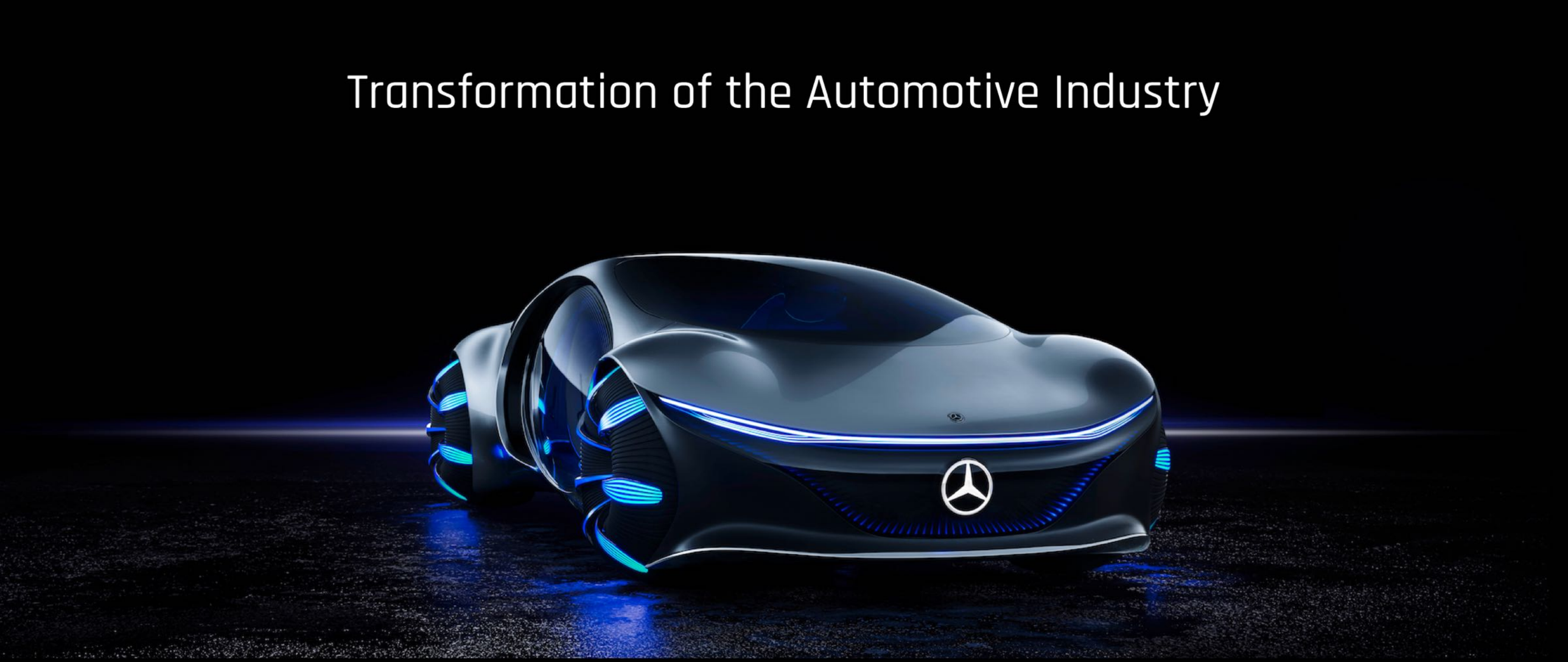
Transformation of the Automotive Industry

Transformation of the Automotive Industry



Connected

Seamless
Mobility

Autonomous

More Comfort
More Safety

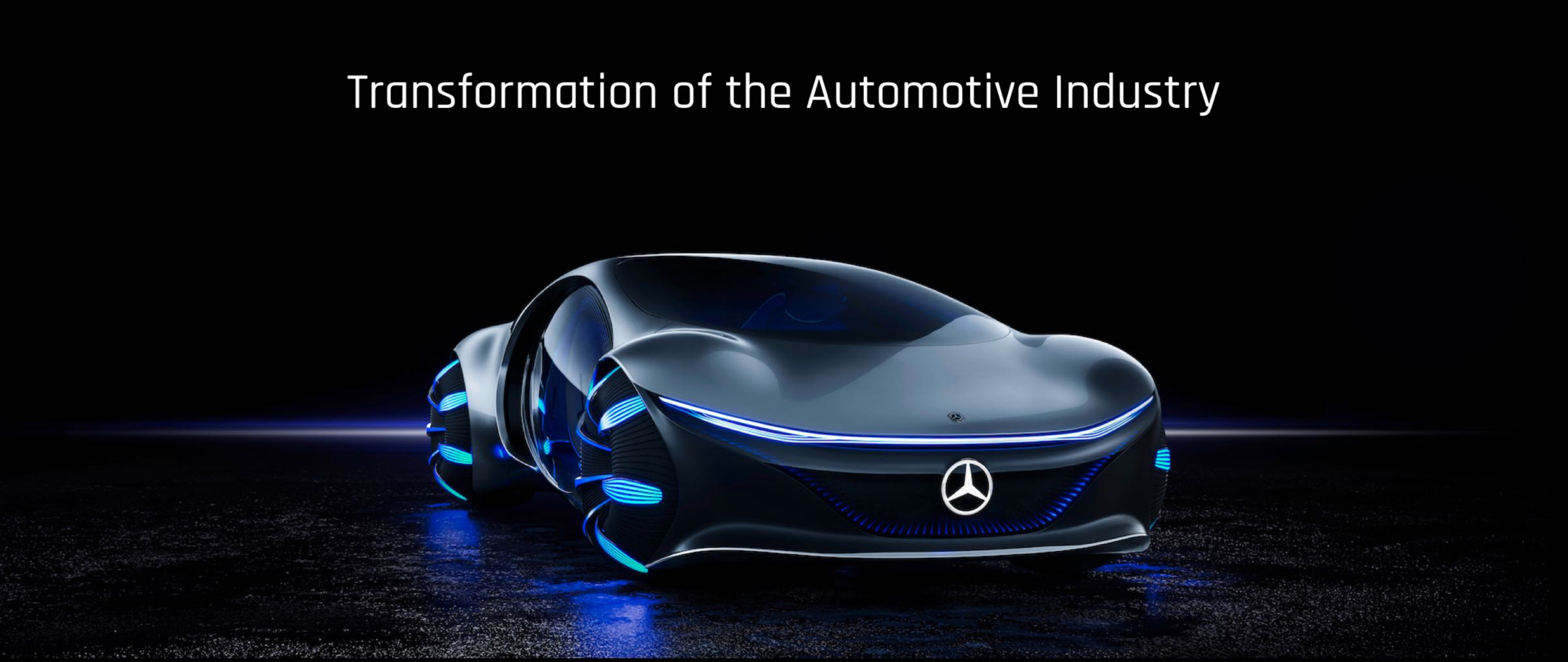
Shared & Services

New Services
With MercedesMe

Electric

Emision Free
Mobility

Transformation of the Automotive Industry



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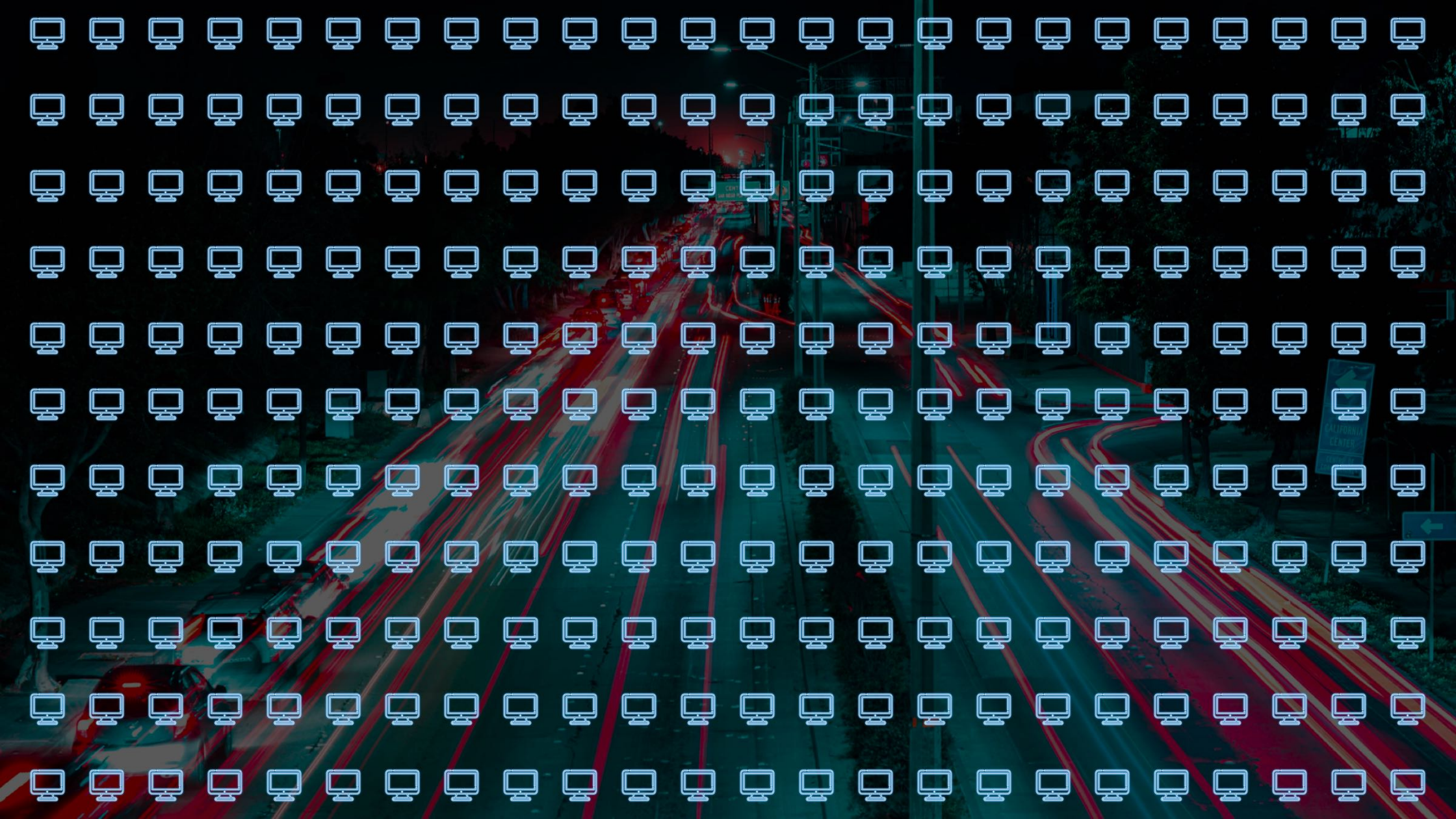
Securing the Connected Car & Defending a Fleet


CENTRO
BAJA
CALIFORNIA
CENTER
CENTRO DE
CONVENCIONES



FleetSecOps: Ongoing Fleet Defense

Cars on the Road

FleetSecOps: Ongoing Fleet Defense

Cars on the Road

Who We Are

- Skyo-Go Team is a security research team established in 2014
- Focus on Connected Cars, Industry Security
- 75% market share on Cybersecurity of Connected Cars in China
- Notable Researches
  - 2014 Tesla & BYD Connectivity Functionality
  - 2016 Tesla Autopilot System
  - 2017 CAN-Pick (CAN-Bus evaluation platform, published in Black Hat USA 2017)
  - 2018 VADS (Vehicle Active Defense System for CAN-bus)
  - 2019 Mercedes-Benz: From Hardware to Control

SKY-GO
360 VEHICLE CYBER SECURITY

Timeline

- July 16, 2018: Start Reverse Engineering on Mercedes-Benz Cars (360)
- Aug 21, 2019: The findings reported to Daimler (360)
- Aug 23, 2019: The services shutdown: preventing further effect on MB cars (Mercedes-Benz)
- Aug 26, 2019: Initial fix (Mercedes-Benz)
- Sep 12, 2019: All access vulnerabilities fixed (Mercedes-Benz)
- Oct 23, 2019: Joint workshop (360 & Mercedes-Benz)
- Aug 06, 2020: Black Hat USA Publication (360 & Mercedes-Benz)

Result of Our Research

- Impact all Mercedes-Benz connected cars in China over **2 millions**.
- Get access to invoke remote service to control the car, like control the doors, lights, windows, engines without physical access.

Agenda

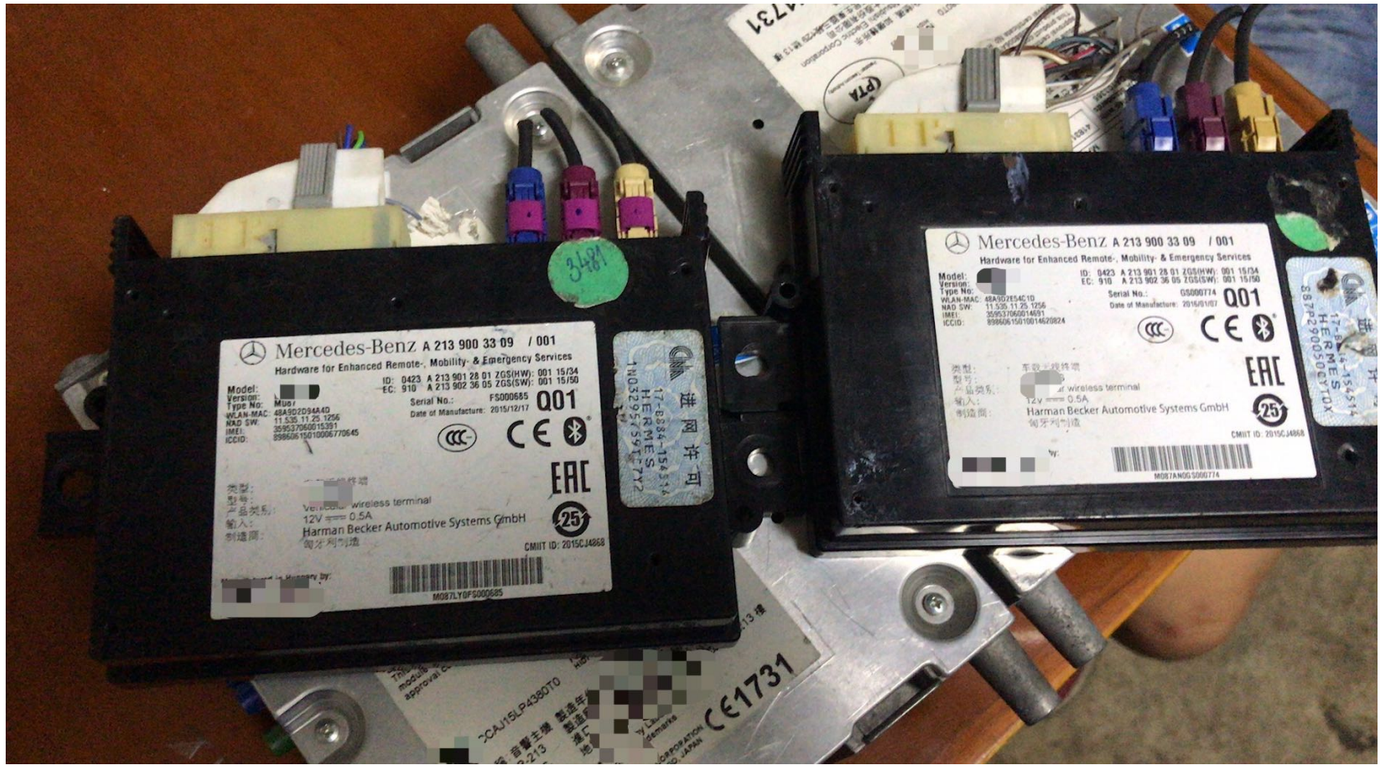
- Build Testbench
- HERMES Jailbreak
- Way to Car Control
- Summary from Sky-Go
- Incident Response
- Summary from Mercedes-Benz

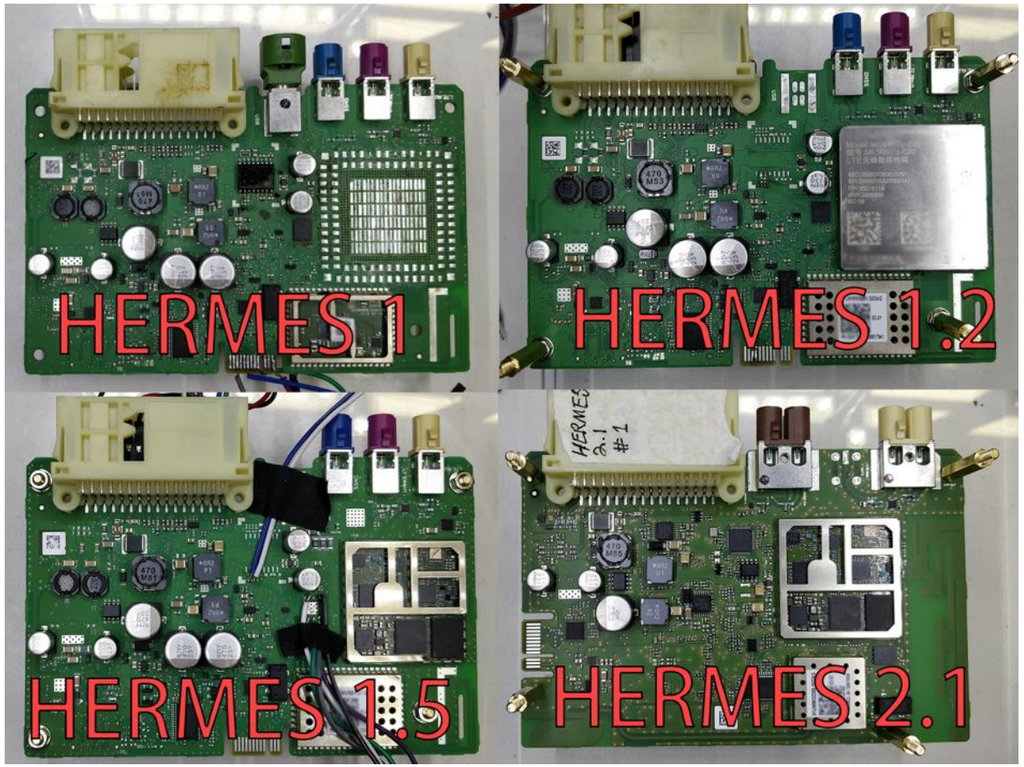
Build Testbench

Key components

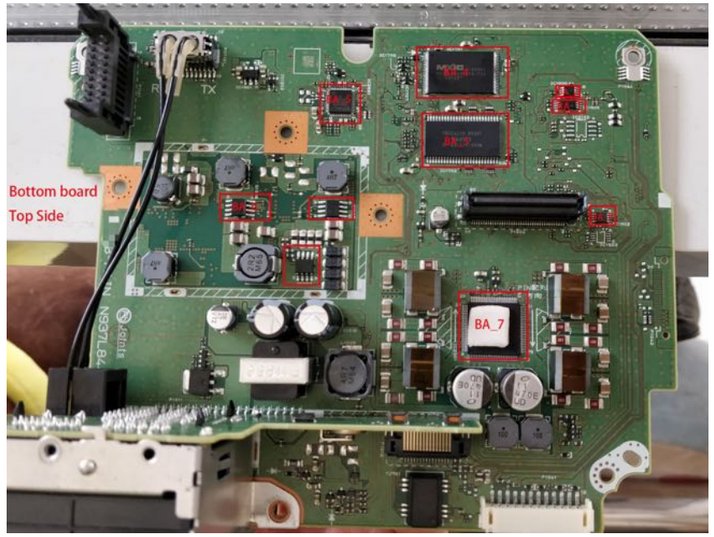
- HERMES (a.k.a. TCU)
- Head-Unit (a.k.a. IVI)

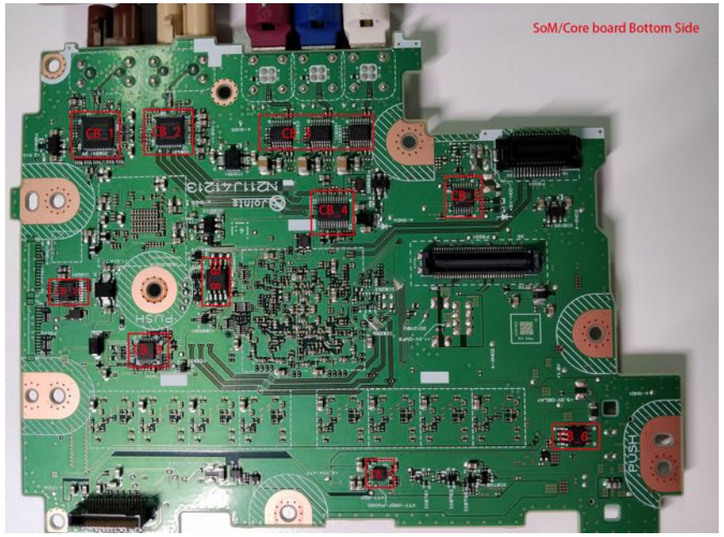
Test devices – HERMES

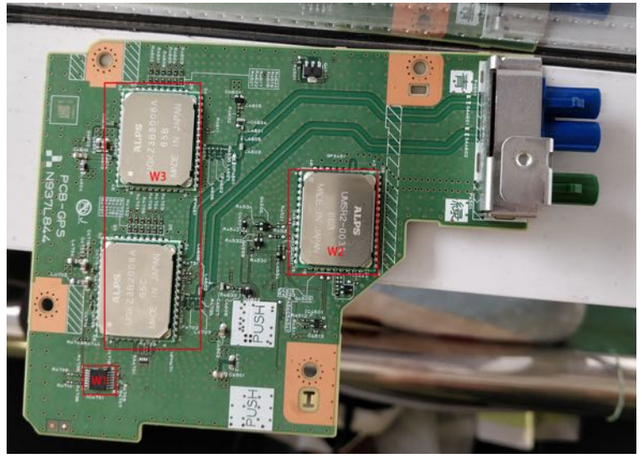


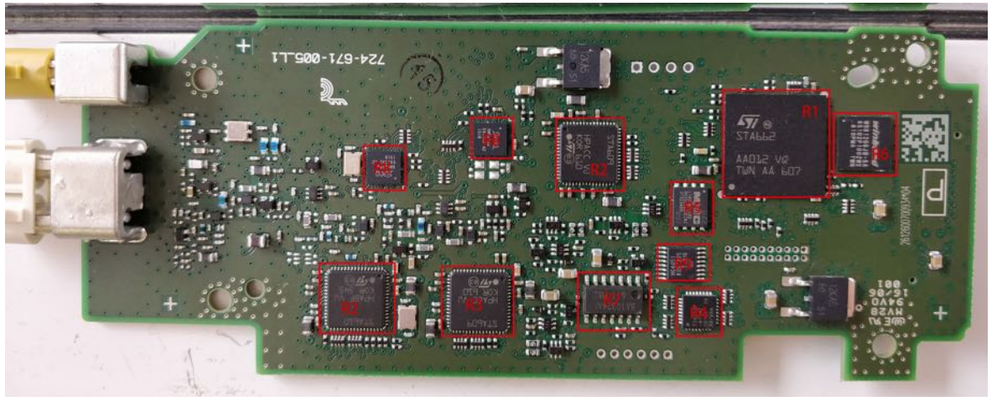


Test devices – Head-Unit

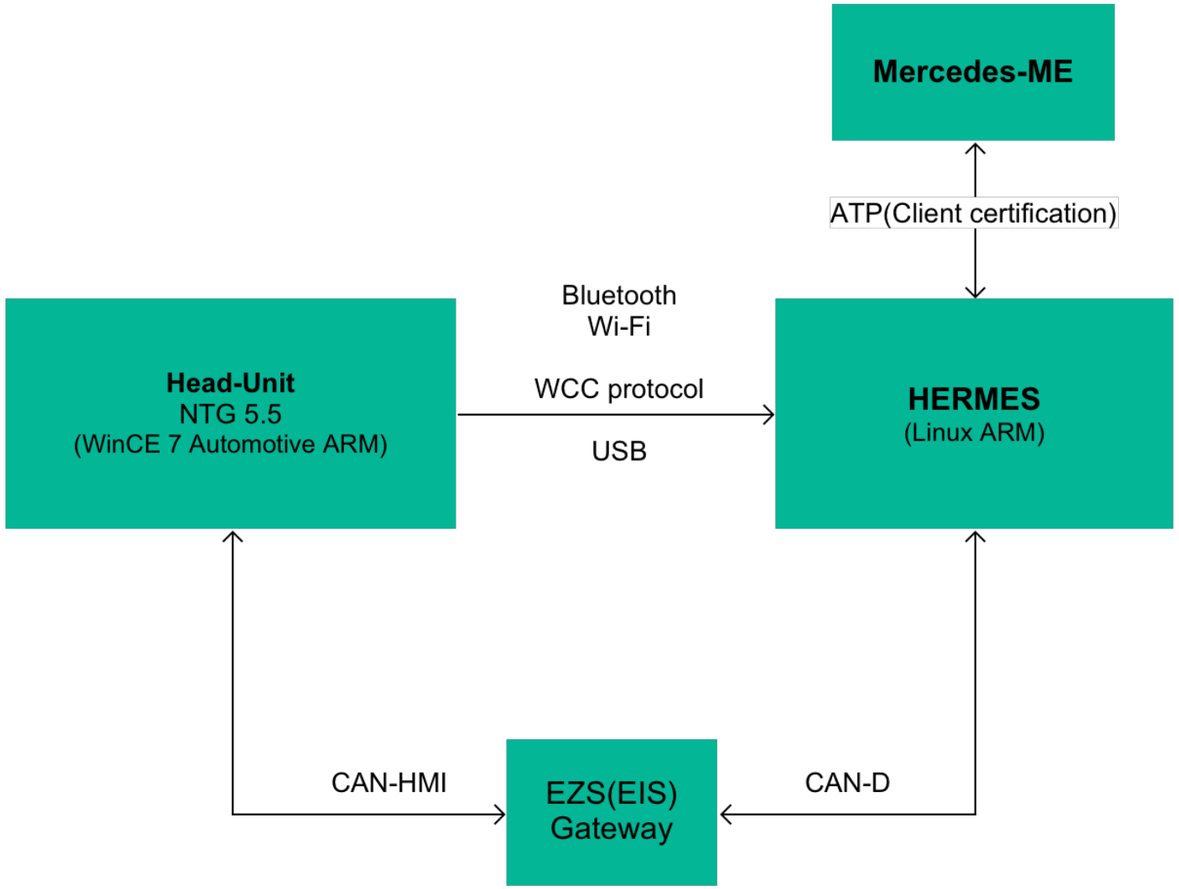


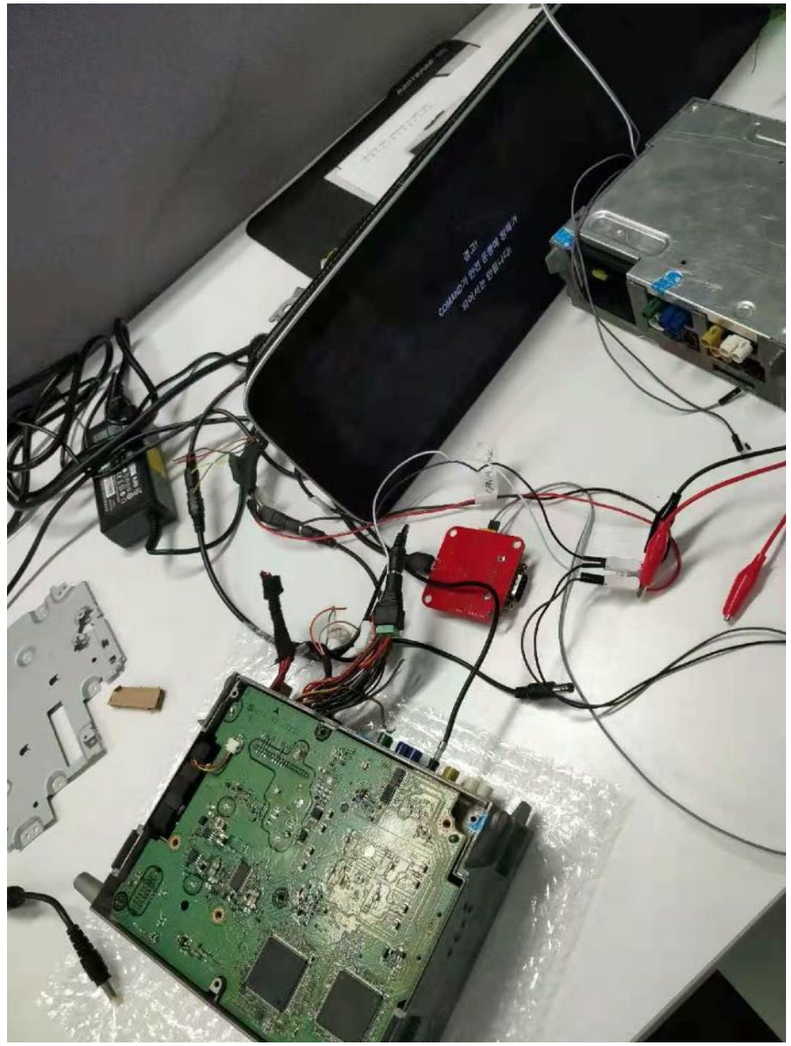






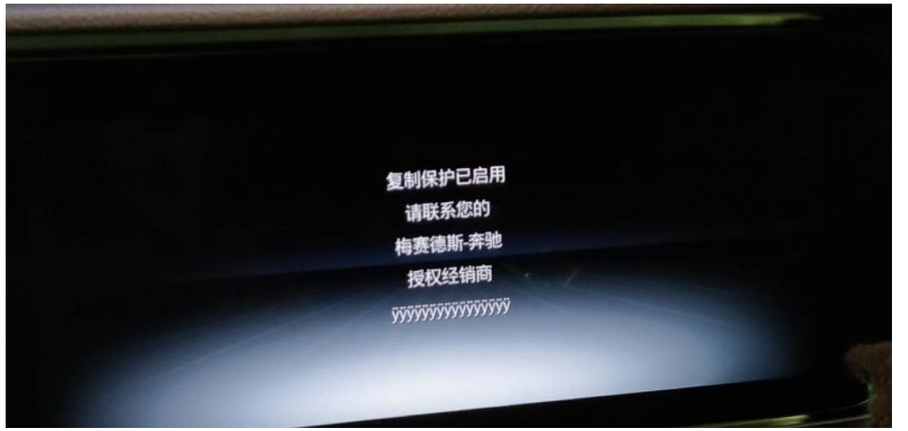
Testbench on Table





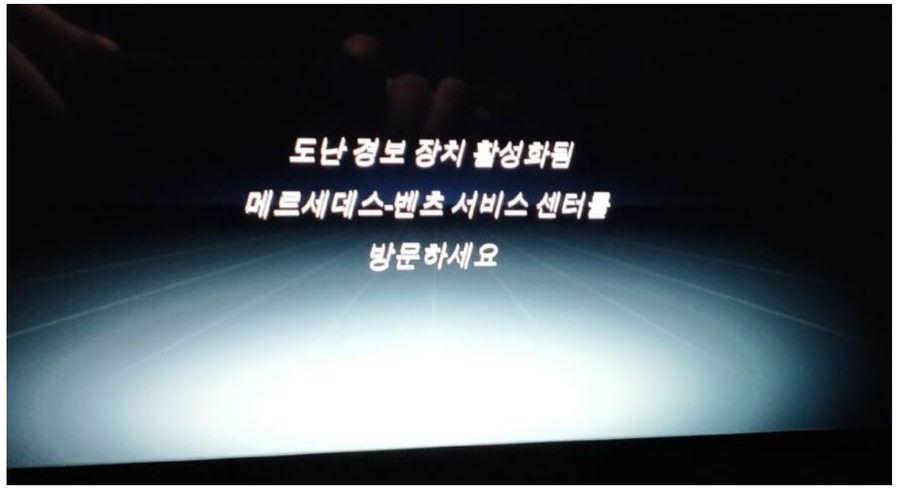
Against with the anti-theft

- Varieties of anti-theft warning.
- Our goal is to start the Head-Unit.



Copy-protection Warning

Anti-theft protection, please restart



Anti-theft protection

Against with the anti-theft

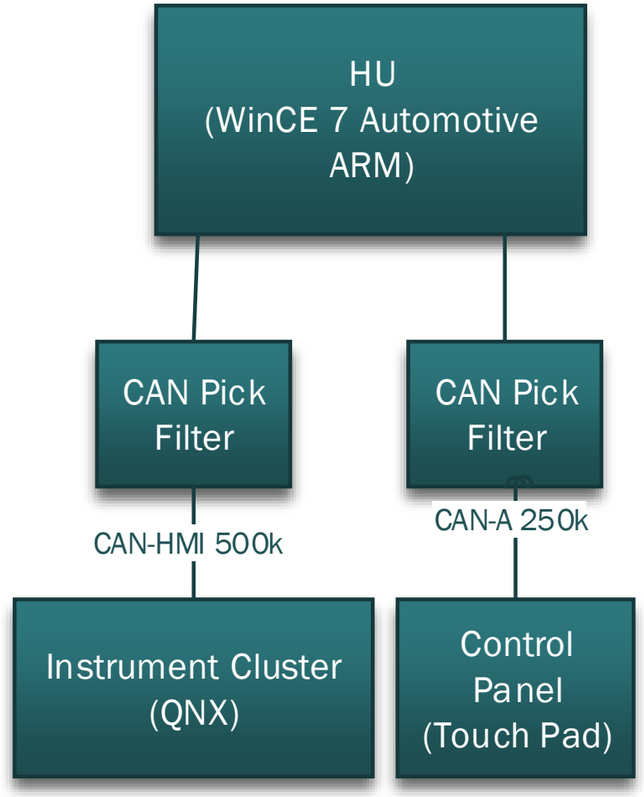
- Ask your dealer to remove the anti-theft lock.
- You need
  - Service fee each time $100
  - Reservation
  - Time

Xentry + SD Connect

Against with the anti-theft

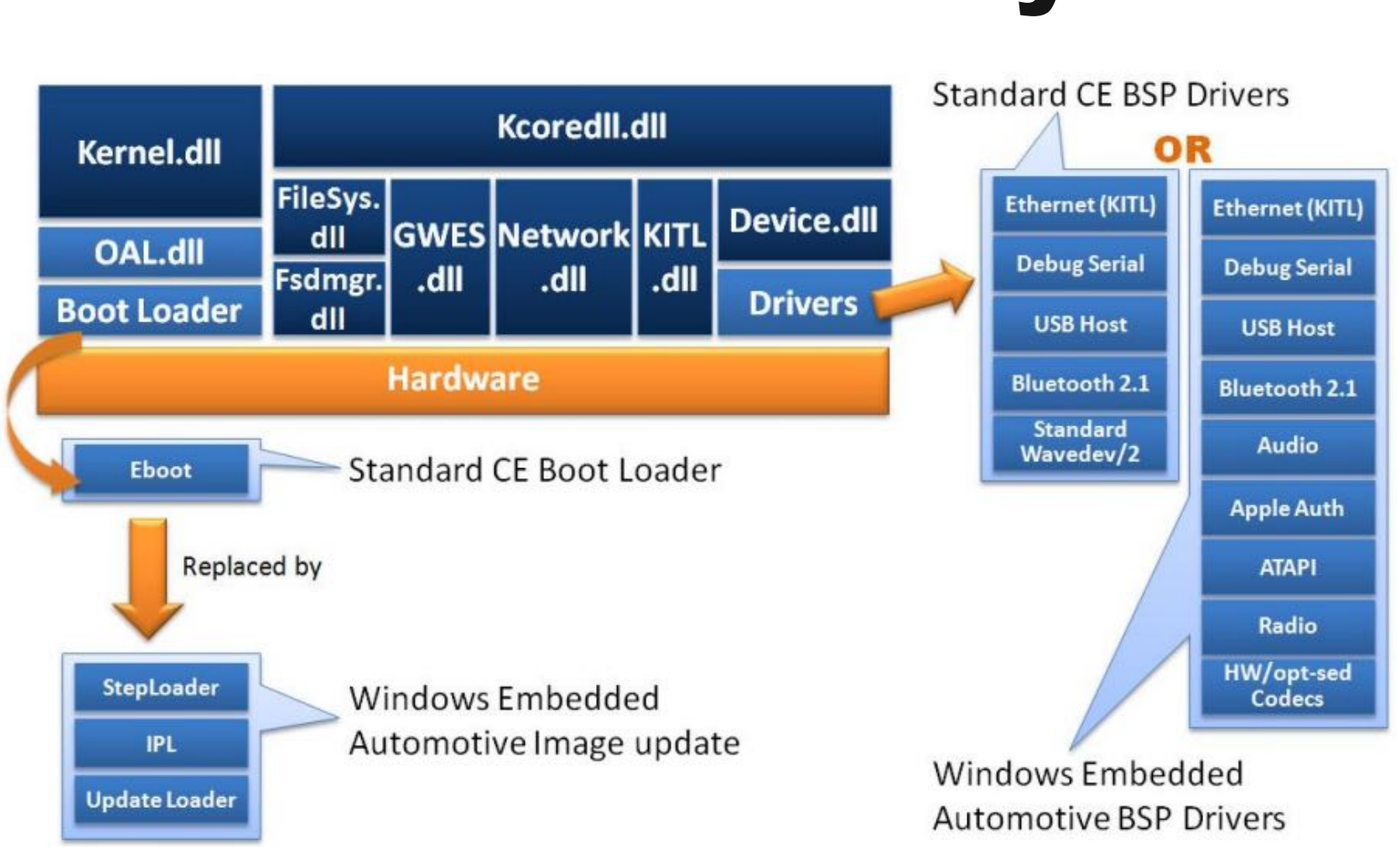
- Backup the SD-card.
- Using CAN-bus toolkit to find out the anti-theft trigger message.

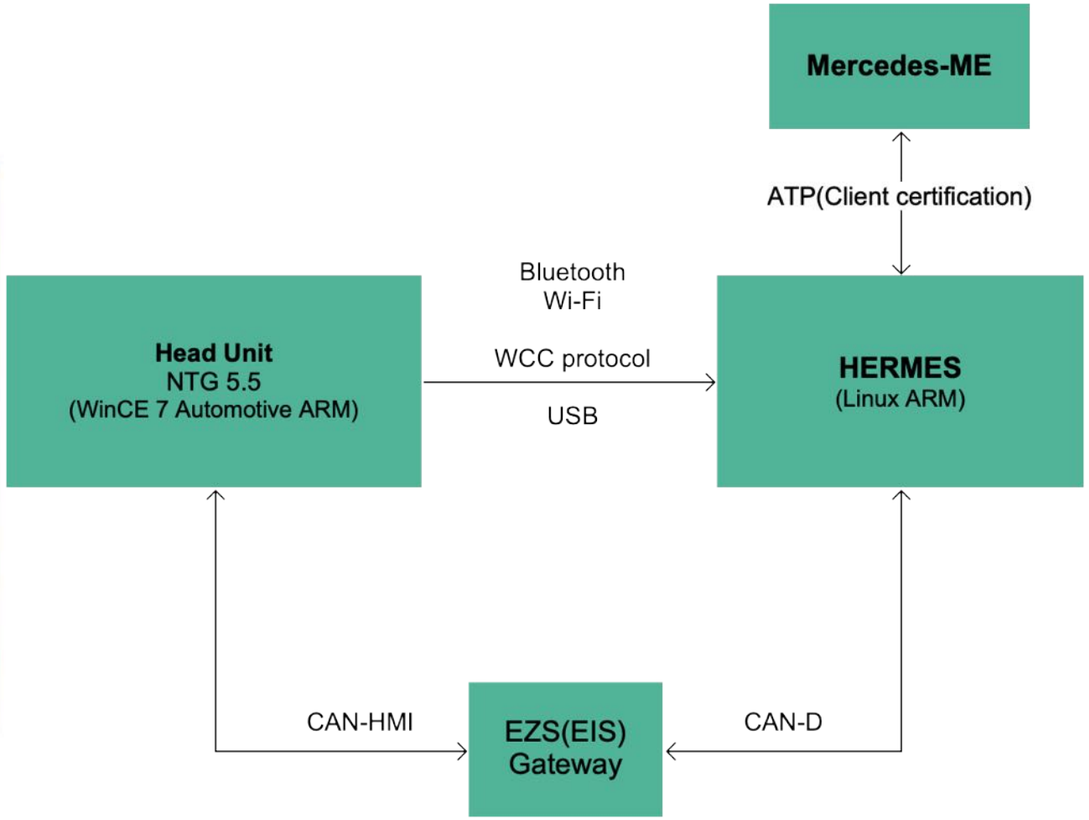
Heart beats



CAN-bus MITM Diagram

Attack Vector Analysis

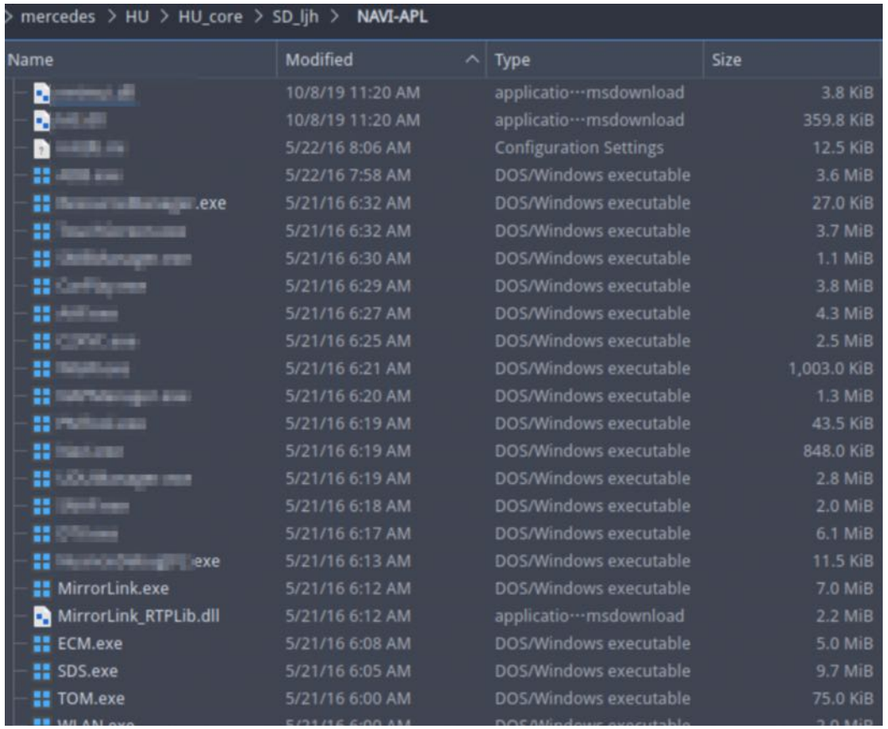




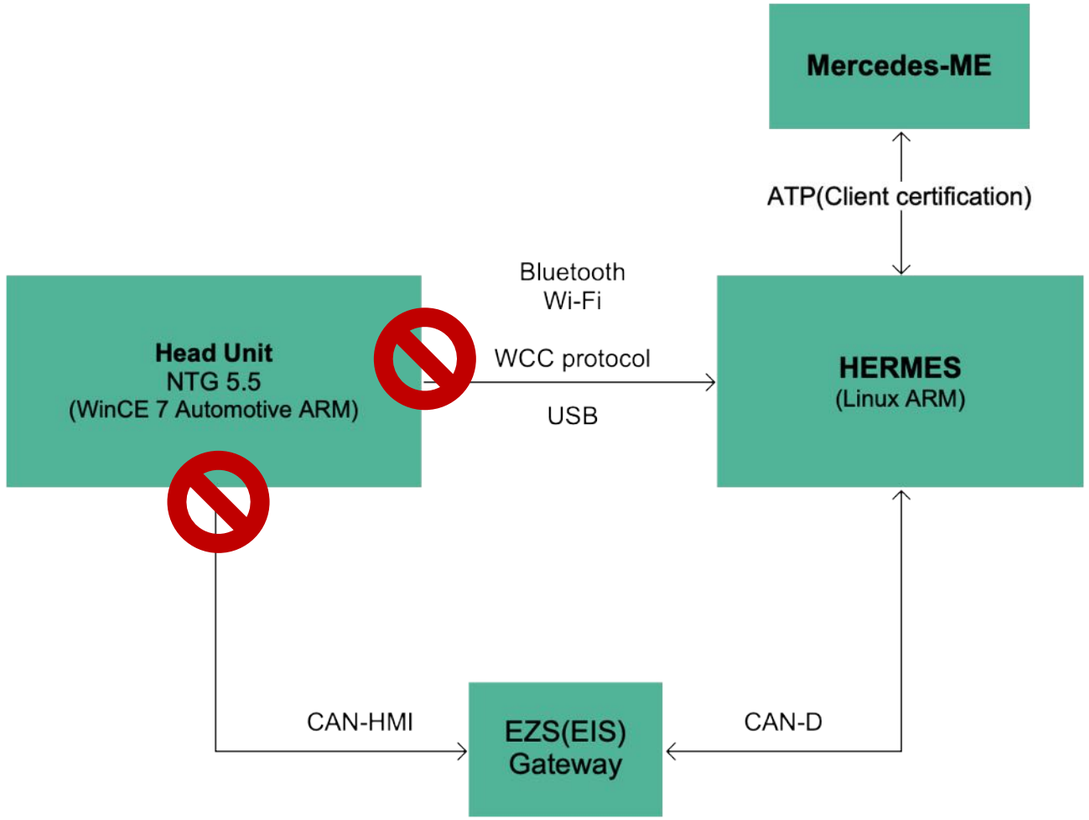
Windows CE Automotive System Architecture

Attack Vector Analysis

- Head-Unit
  - Windows CE Automotive 7 is so hard



Executable files in Head Unit

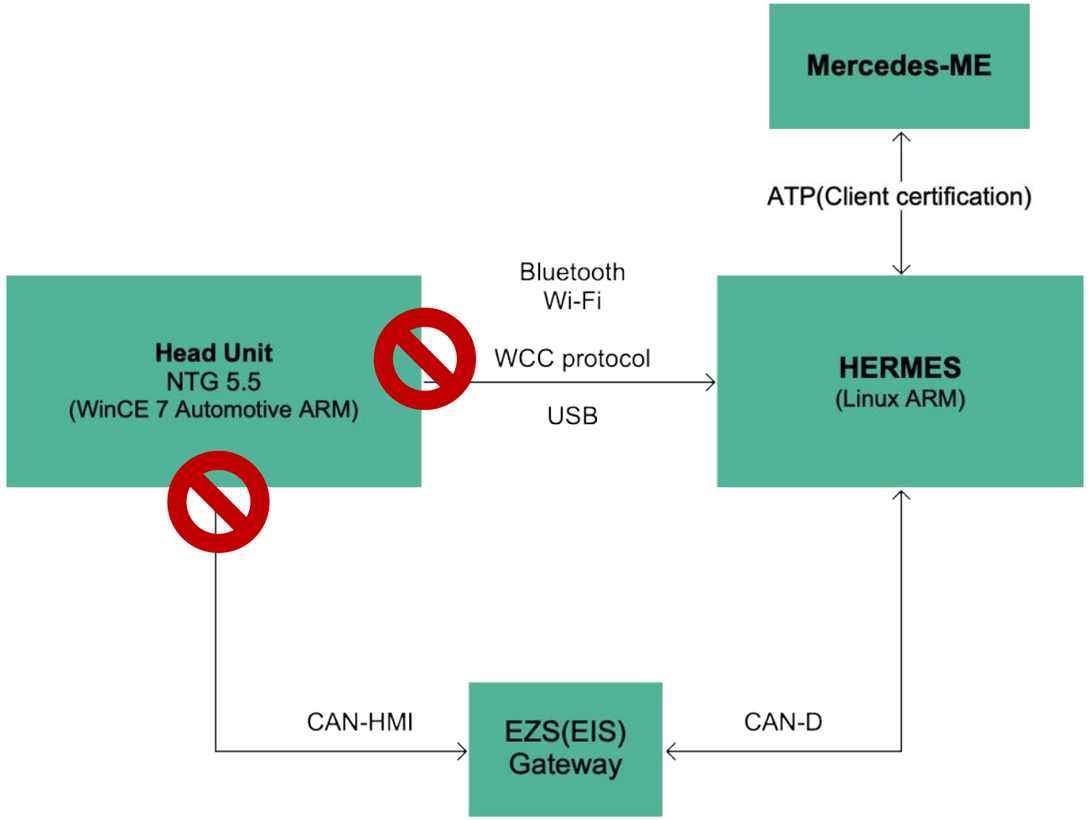


Attack Vector Analysis

- Head-Unit
  - Windows CE Automotive 7 is so hard
  - Without source code
  - Without debug environment

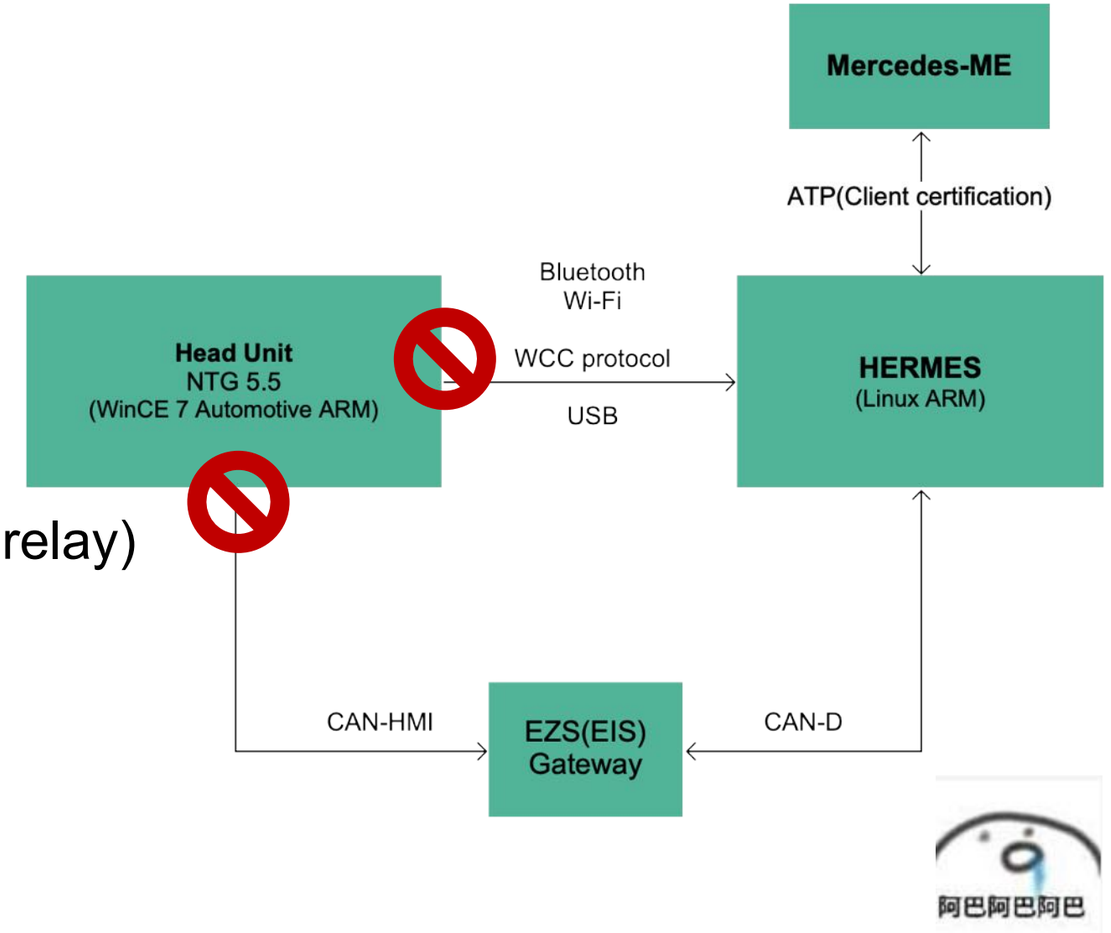
```
$ python ./nb0_dumper.py NAVI-APL.img_decompressed

name            offset          load_addr       toc_p           toc_offset
0x00000000      0x00000000      0x80000000      0x8C069BD8      0x0C069BD8



dllfirst        dlllast         physfirst       physlast        nummods         ulRAMStart
sCPUType        usMiscFlags     pExtensions     ulTrackingStart ulTrackingLen
0x4001EE43      0x455FF000      0x80000000      0x8C06F140      0x000001FE      0x8D200000
x000001C2       0x00000002      0x800016D0      0x00000000      0x00000000

dwFileAttributes        ftTime                  nFileSize
0x00000007              2016-05-17 08:51:04     0x00039000
0x00000007              2016-05-17 08:51:05     0x00015000
0x00000007              2016-05-17 08:18:11     0x00056000
0x00001007              2016-05-17 09:00:33     0x000B1000
0x00000007              2016-05-17 08:26:23     0x00005000
0x00000007              2016-05-17 08:18:01     0x00099000
```

Kernel file



Attack Vector Analysis

- Head-Unit
  - Windows CE Automotive 7 is so hard
  - Without source code
  - Without debug environment
- OBD (EZS, CAN-D)
  - Physical access
  - The FBS4 can’t be attack yet.(Maybe with key-fob relay)
  - Upgrade package has signature protection.



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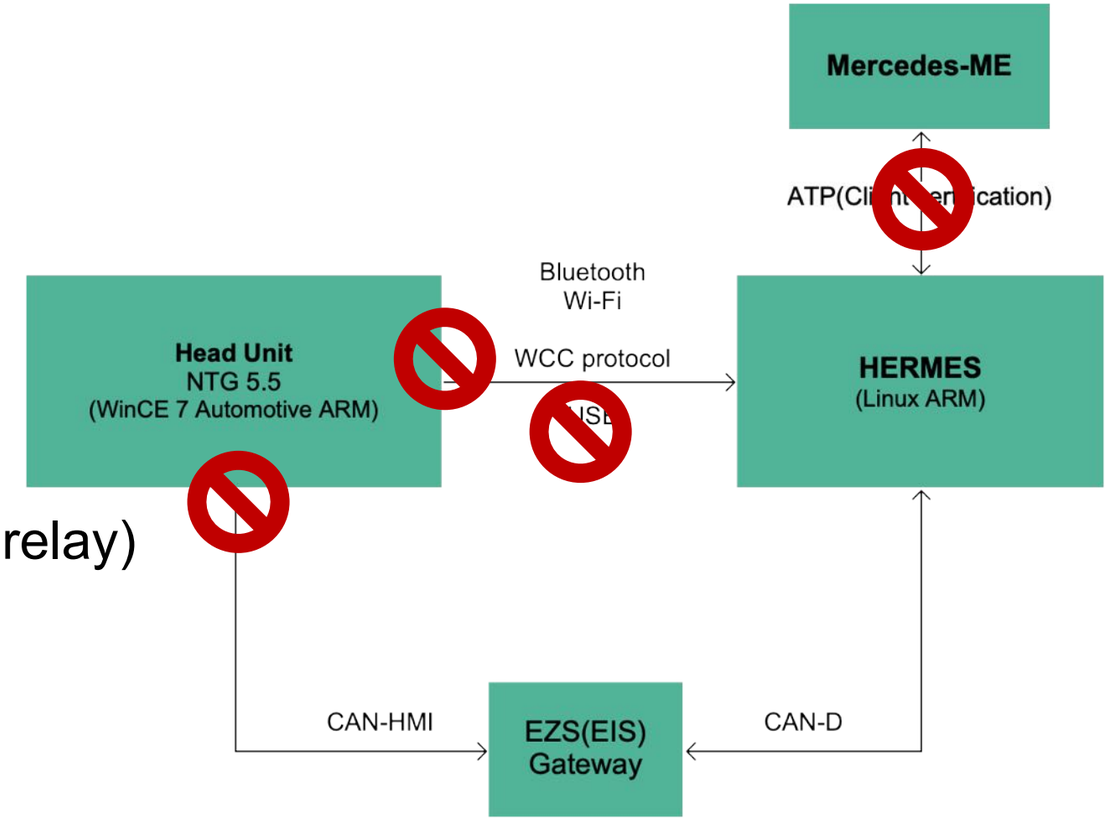
- HERMES
  - Embedded Linux
  - Telematics



Attack Vector Analysis

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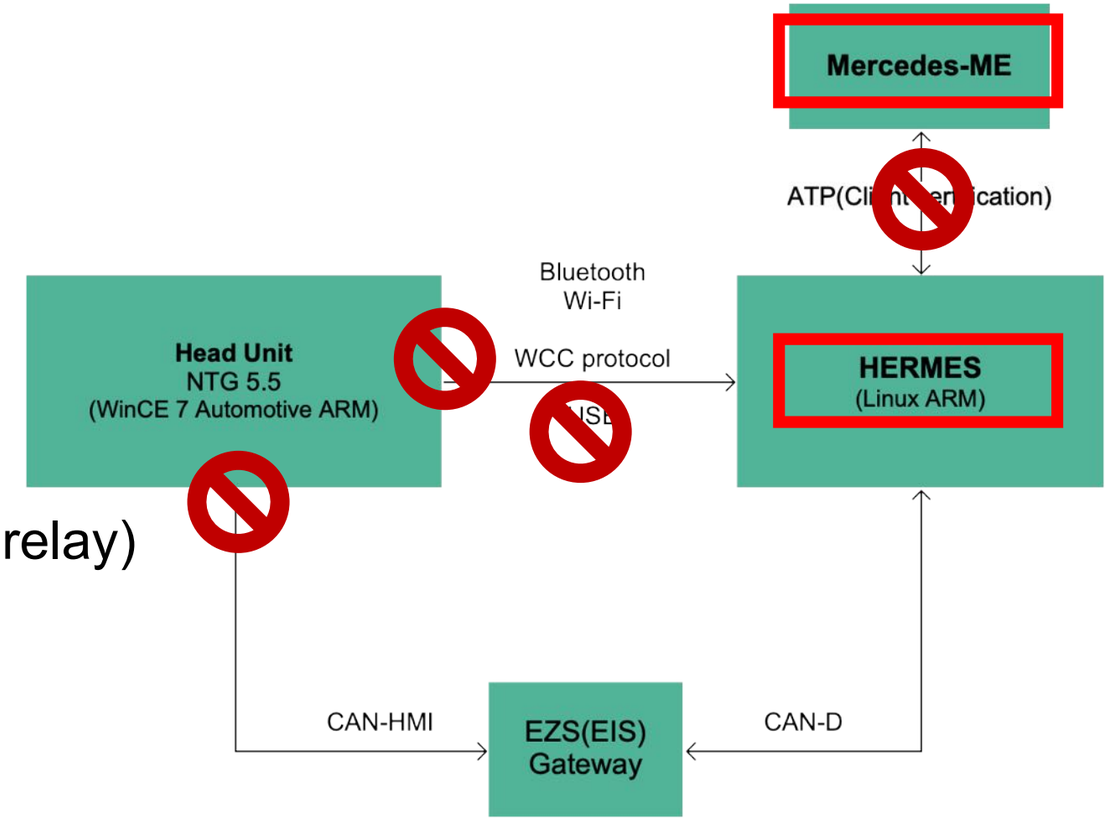
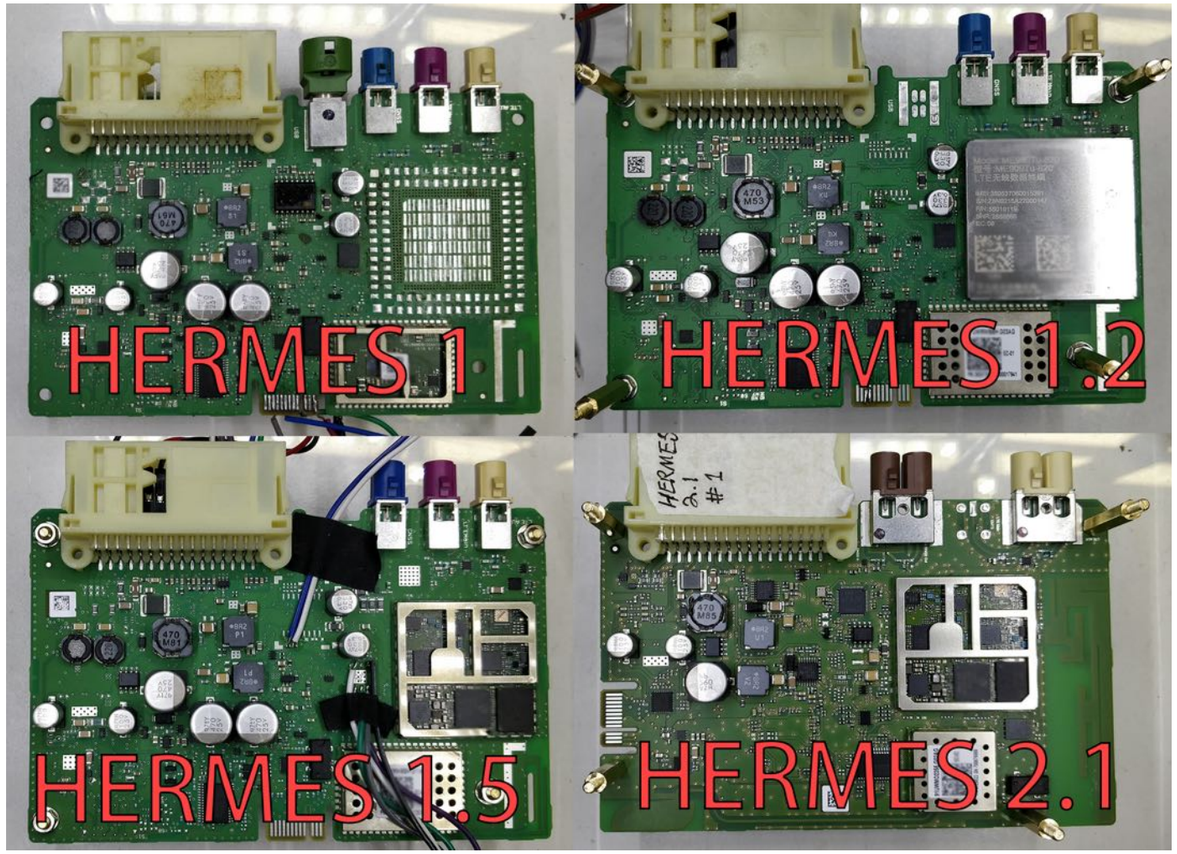
- HERMES
  - Embedded Linux
  - Telematics
  - 4G attacking is useless for it



HERMES Jailbreak

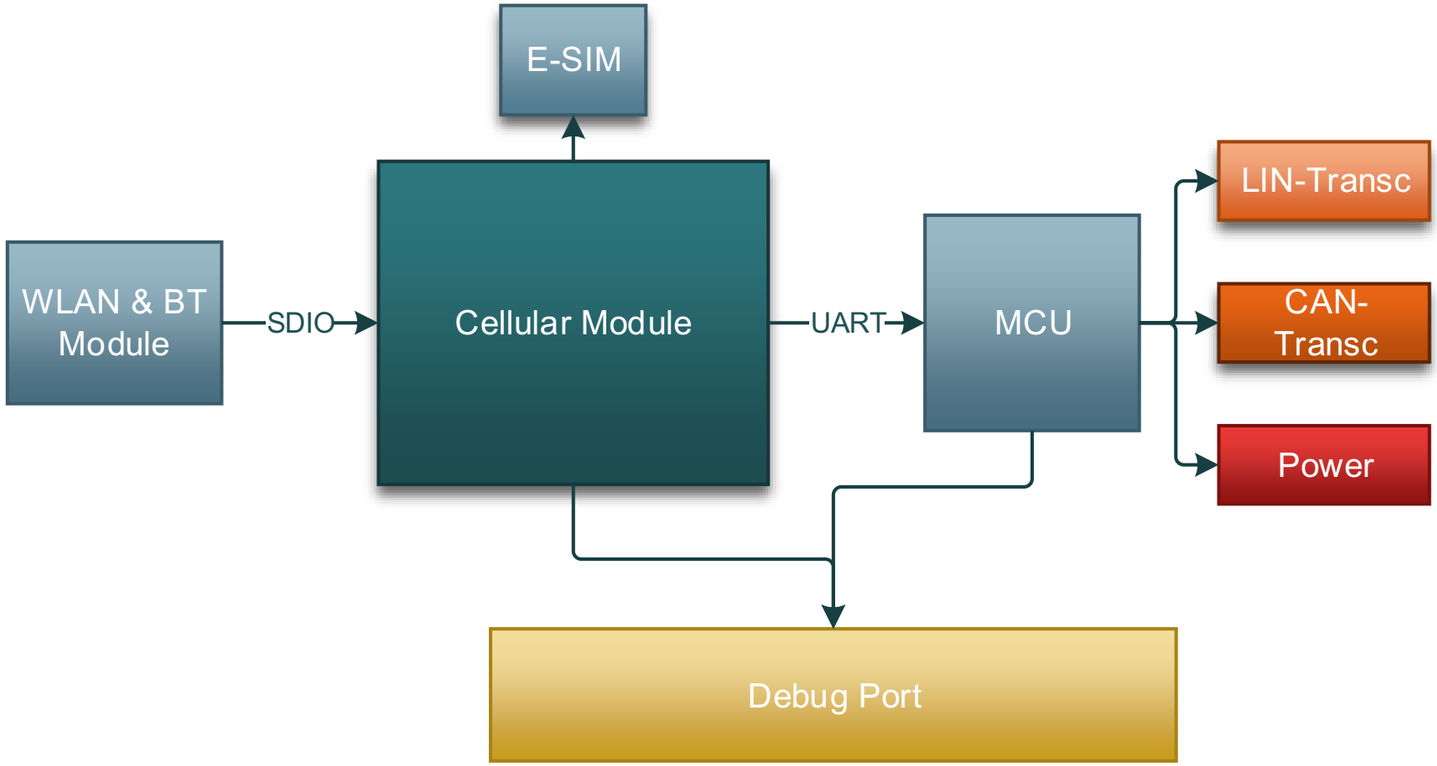
HERMES Version Design Comparison

- HERMES 1
  - USB Cable
  - ME909Tu LTE
  - MU809Tu UTMS
- HERMES 1.5
  - ME919bs
- HERMES 2.1
  - ME919bs



Finding Peripheral Interfaces

- UART
- USB NAD
- JTAG (reversed)

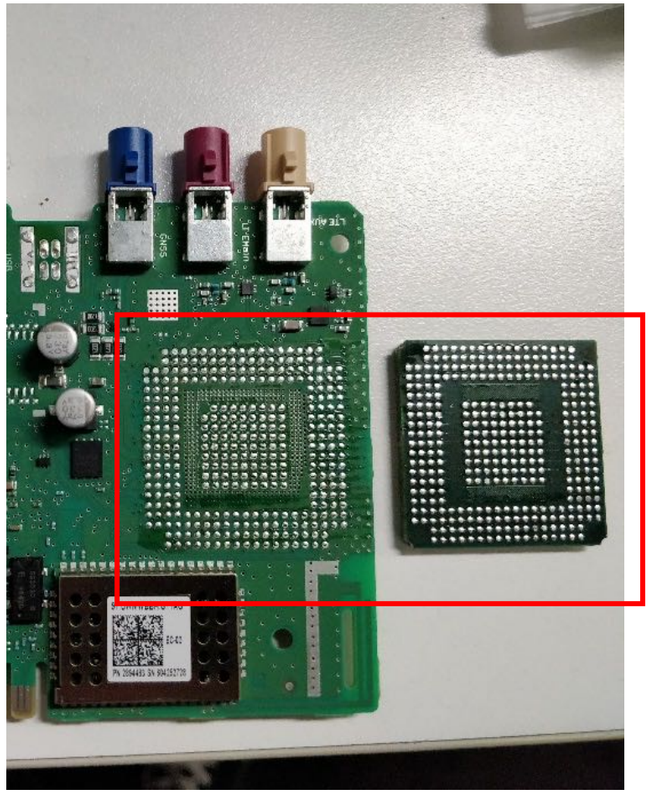


HERMES Components Block Diagram

Way to Car Control

Finding Peripheral Interfaces

- The packaging is LGA, it’s hard to teardown.
- To check out the debug interfaces pinout.
  - Multimeter
  - Flashlight
  - X-Ray



UART Debug Port

- APN Configurations (Only activated TCU)
- TSP Back-end configurations.

```
]:  [info:] Loaded APN1 settings: URL: "[redacted].CLFU.NJM2MAPN", user: [redacted]
]:  [info:] APN with index 2 is not configured
]:  [info:] Loaded APN2 settings: URL: "[redacted].CLFU.NJM2MAPN", user: [redacted]
]:  [info:] APN with index 3 is not configured
]:  [info:] Loaded APN3 settings: URL: "DefaultValue1", user: [redacted]
]:  [info:] APN with index 4 is not configured
]:  [info:] Loaded APN4 settings: URL: "DefaultValue2", user: [redacted]
]:  [info:] >>>>[getInstance][6973]
```

APN initialization log

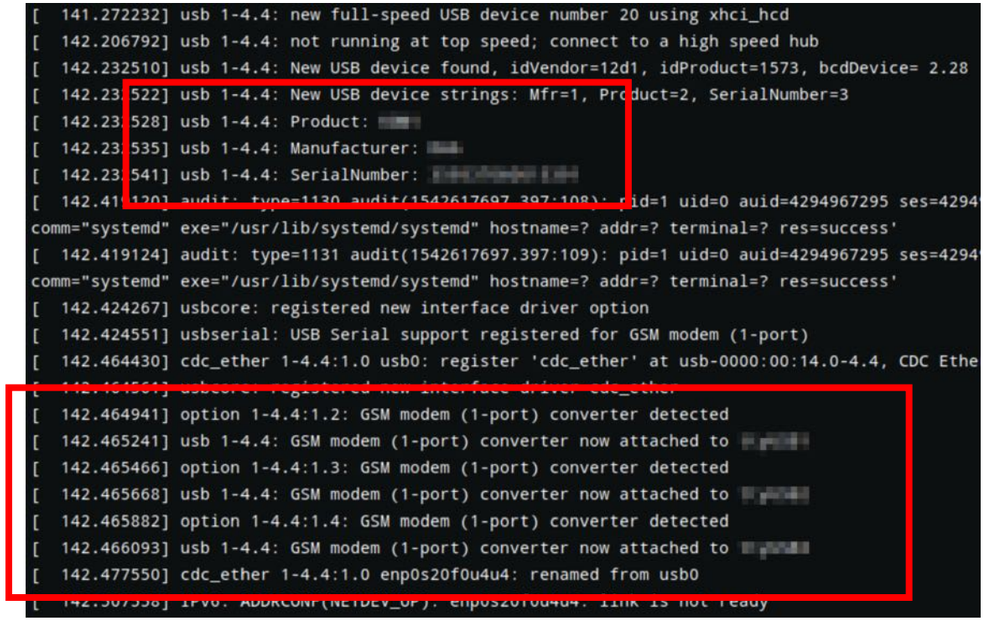
```
27 05:36:48.395 OMADM[1052]:  [info:] TCUReadCb read value [https:/[redacted]] for param [redacted]URL]
27 05:36:48.395 OMADM[1052]:  [info:] TCUReadCb read value [https://[redacted]] for param [redacted]]
27 05:36:48.395 OMADM[1052]:  [info:] TCUReadCb read value [https://[redacted]] for param [redacted]]
27 05:36:48.396 OMADM[1052]:  [info:] TCUReadCb read value [https://[redacted]] for param [redacted]RL]
27 05:36:48.399 OMADM[1052]:  [info:] TCUReadCb read value [https://[redacted]] for param [redacted]RL]
27 05:36:48.399 OMADM[1052]:  [info:] TCUReadCb read value [https://[redacted]] for param [redacted]URL]
27 05:36:48.399 OMADM[1052]:  [info:] TCUReadCb read value [https:/[redacted]] for param [redacted]URL]
```

Back-end requests log

USB Mode Switching

- AT^SETMODE is default ECM

- AT^SETMODE=3 for RNDIS ADB
- ttyUSB0 Application
- ttyUSB1 PCUI
- ttyUSB2 serialB
- ttyUSB3 serial



USB log

6 Interfaces in Windows Device MGMT

USB Debug Mode

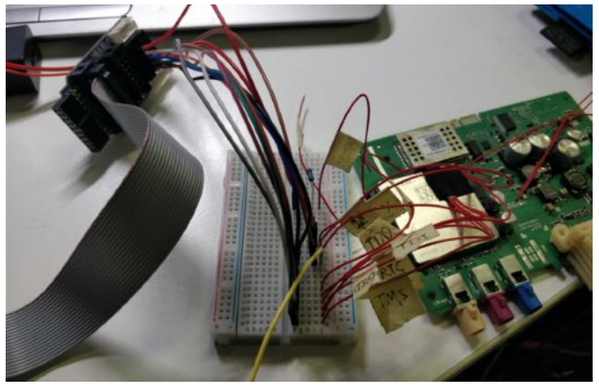
- To obtain APN configurations.
- AT^GODLOAD for upgrading flash the filesystem
  - Disable the watchdog first
  - Repackage the firmware

```
OK
AT+CGDCONT?
+CGDCONT: 1,"IPV4V6",'[redacted]CLFU.NJM2MAPN","0.0.0.0",0,0
+CGDCONT: 15,"IP",[redacted].CLFU.NJM2MAPN","0.0.0.0",0,0
+CGDCOAT+CIND?
+CIND: 0,2,1,1,0,0,1,0
```

PDP Context Configuration

On-Chip Debugging

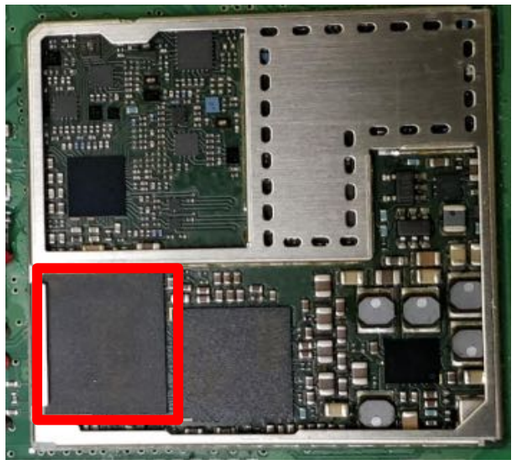
- We can’t enter the Qualcomm EDL mode to read firmware. So we try the OCD.
- Use the OpenOCD with FT2232 to operate the debug interface
  - Disable the watchdog
  - Reverse analyze the NAND Controller Driver (Or use QDLoader)



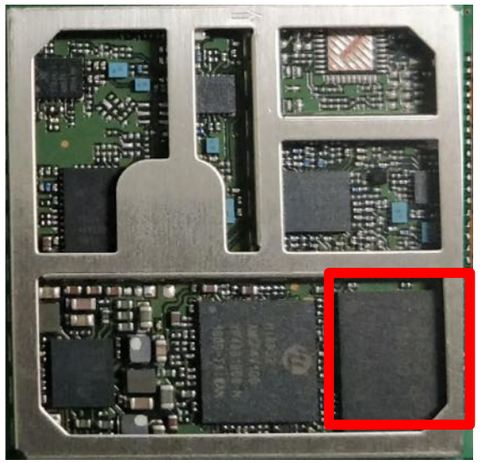
Connect JTAG pin to FT2232

Dumping NAND flash

- The Cellular Module has an eMCP NAND



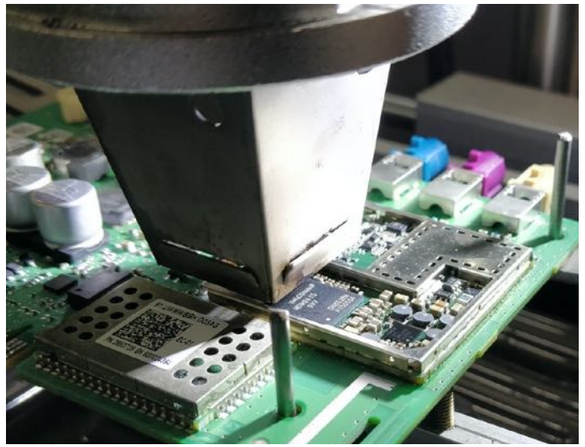
Old Cellular Module



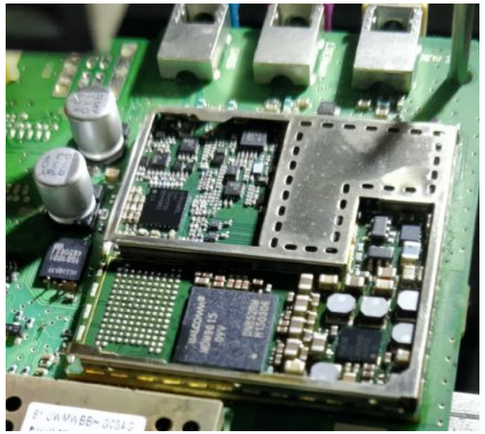
New Cellular Module

Dumping NAND flash

- Tear down the flash chip with BGA rework station



400 ℃ Hotair with Infrared Heating

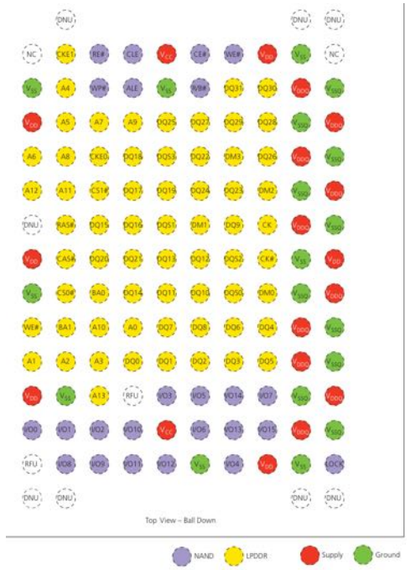


Qualcomm eMCP

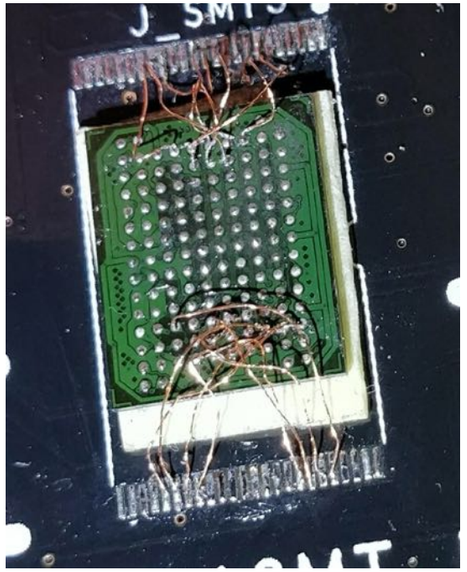
Hisilicon eMCP

Raw NAND Pinout

- The eMCP flash on old cellular module is the BGA 137 footprint.
- 6-ways Control pins & 8-bit Data I/O pins



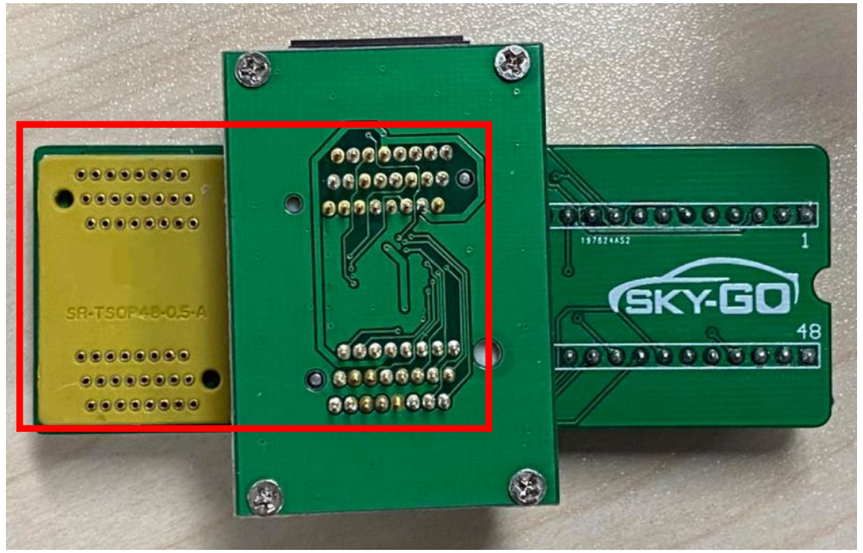
BGA 137 Pin-Assignment



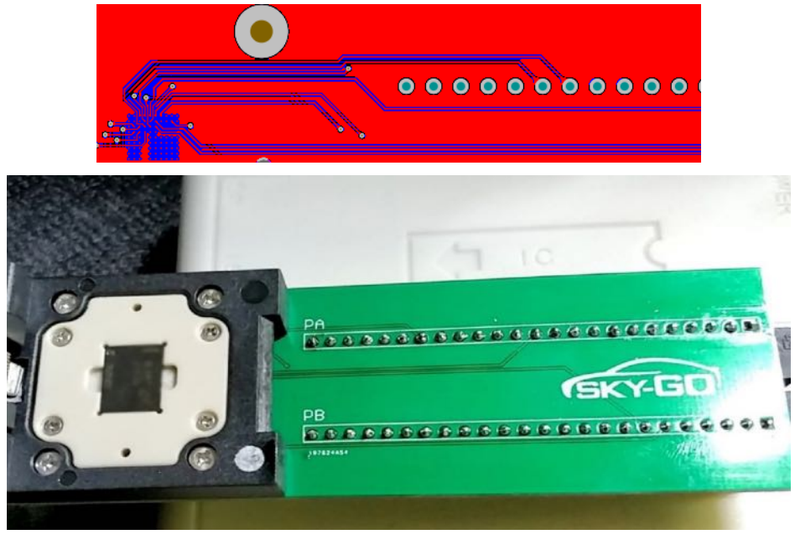
Wiring up with magnet wire

Dumping Firmware with BGA Socket

- We made some sockets and adaptors for these NAND Flash.
- The socket and adapter are separate designs.



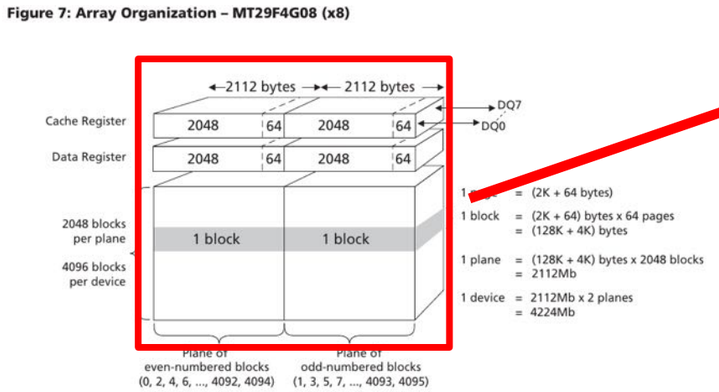
Full pinout adaptor



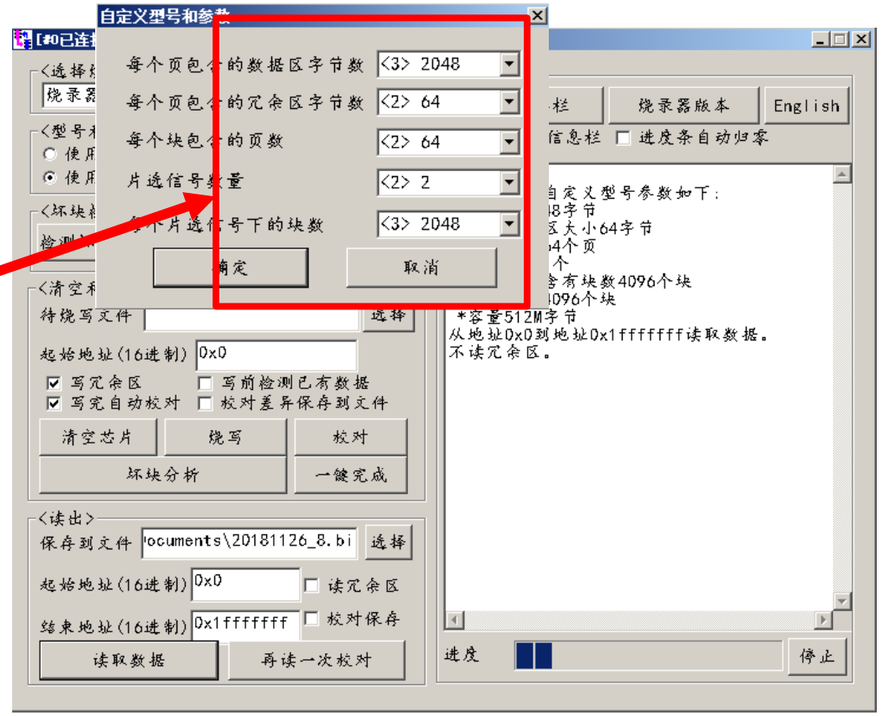
BGA Socket

Reading NAND Flash Data

- 2048-Bytes Data + 64-bytes Spare Area
- The NAND chip size is 512MB



NAND Array Organization



PROMAN NAND reader

Finding Spare Area

- The NAND user manual has suggestions for spare area mapping.
- In general, the spare area mapping always defined by NAND drivers.

Max Byte Address	Min Byte Address	ECC Protected	Area	Description
1FFh	000h	Yes	Main 0	User data
3FFh	200h	Yes	Main 1	User data
5FFh	400h	Yes	Main 2	User data
7FFh	600h	Yes	Main 3	User data
801h	800h	No		Reserved
803h	802h	No		User metadata II
807h	804h	Yes	Spare 0	User metadata I
80Fh	808h	Yes	Spare 0	ECC for main/spare 0
811h	810h	No		Reserved
813h	812h	No		User metadata II
817h	814h	Yes	Spare 1	User metadata I
81Fh	818h	Yes	Spare 1	ECC for main/spare 1
821h	820h	No		Reserved
823h	822h	No		User metadata II
827h	824h	Yes	Spare 2	User metadata I
82Fh	828h	Yes	Spare 2	ECC for main/spare 2
831h	830h	No		User data
833h	832h	No		User metadata II
837h	834h	Yes	Spare 3	User metadata I
83Fh	838h	Yes	Spare 3	ECC for main/spare 3

Bad Block Information	ECC Parity	User Data (Metadata)
2 bytes	8 bytes	6 bytes

Spare area mapping (x8)

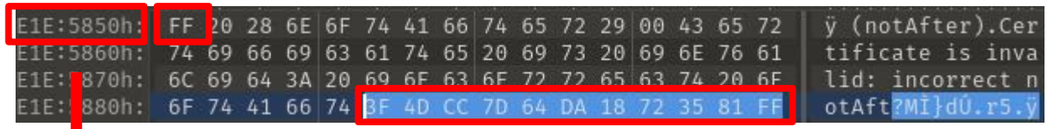
Finding Spare Area

- Two ways to find spare area
  - Checking the source code: /drivers/mtd/nand/qcom_nandc.c

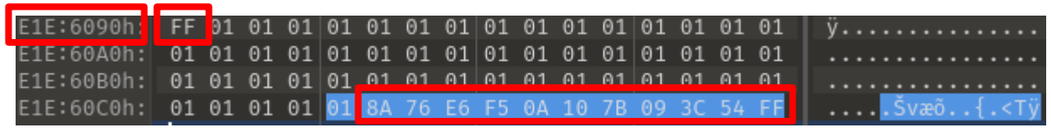
```
2191  * NAND controller page layout info
2192  *
2193  * Layout with ECC enabled:
2194  *
2195  * |----------------------|  |---------------------------------|
2196  * |           xx.......yy|  |             ********xx.......yy|
2197  * |    DATA   xx..ECC..yy|  |    DATA     **SPARE**xx..ECC..yy|
2198  * |   (516)   xx.......yy|  |  (516-n*4)  **(n*4)**xx.......yy|
2199  * |           xx.......yy|  |             ********xx.......yy|
2200  * |----------------------|  |---------------------------------|
2201  *     codeword 1,2..n-1                 codeword n
2202  *  <---(528/532 Bytes)-->    <-------(528/532 Bytes)--------->
2203  *
2204  * n = Number of codewords in the page
2205  * . = ECC bytes
2206  * * = Spare/free bytes
2207  * x = Unused byte(s)
2208  * y = Reserved byte(s)
2209  *
2210  * 2K page: n = 4, spare = 16 bytes
2211  * 4K page: n = 8, spare = 32 bytes
2212  * 8K page: n = 16, spare = 64 bytes
```

Finding Spare Area

- Two ways to find spare area
  - Checking the source code: /drivers/mtd/nand/qcom_nandc.c
  - Comparing NAND pages



The Nst page



The Nst+1 page

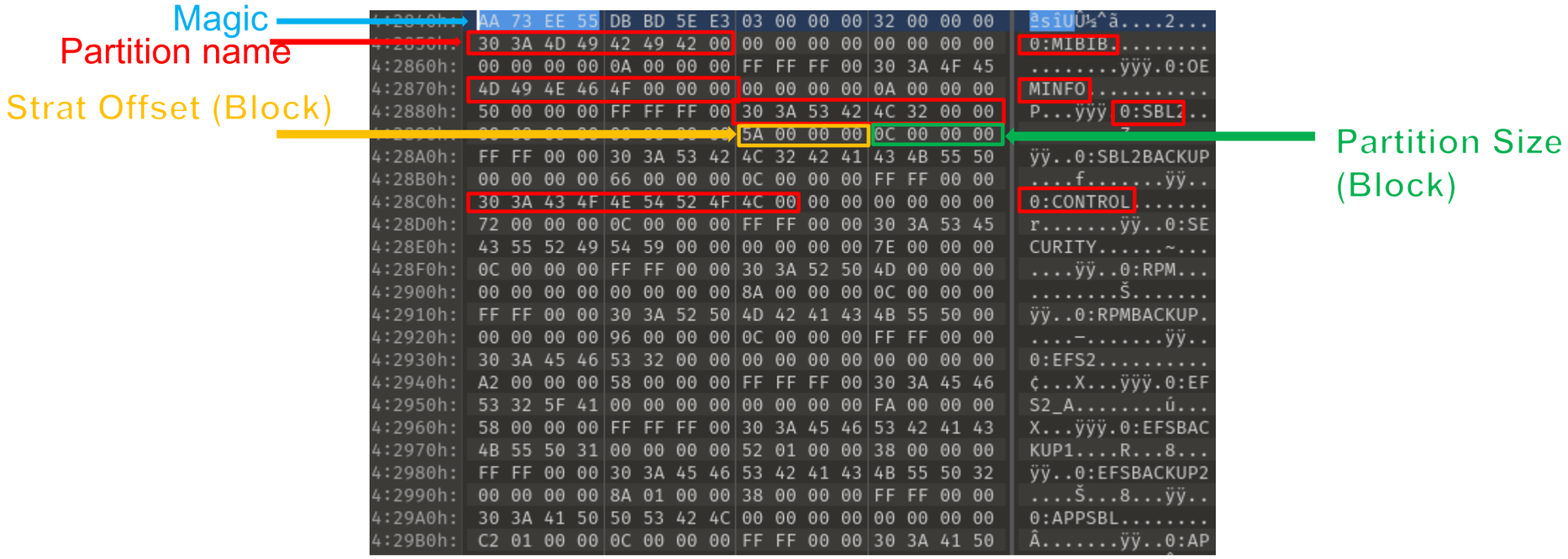
Removing Spare Area

- The spare area are 64-bytes in one page.
- One page has 4 sub-pages. Each sub-page has one ECC area.
- In general, spare area doesn’t include the data zone.

```
try:
    with open(proman_file_path, 'rb') as proman_file:
        promanbin = proman_file.read()
        proman_file.close()
        with open(raw_file_path, 'wb') as raw_file:
            for x in range(0, len(promanbin), 0x840):
                pbuffer = promanbin[x:x+0x840]
                page_a = pbuffer[0x0:0x1D0] + pbuffer[0x1D1:0x1D1+0x34]
                page_b = pbuffer[0x1D1+0x34+0xB:0x3E0] + \
                    pbuffer[0x3E1:0x3E1+0x34]
                page_c = pbuffer[0x3E1+0x34+0xB:0x5F0] + \
                    pbuffer[0x5F1:0x5F1+0x34]
                page_d = pbuffer[0x5F1+0x34+0xB:0x800] + \
                    pbuffer[0x801:0x801+0x24]
                pbuffer = page_a + page_b + page_c + page_d
                raw_file.write(pbuffer)
        raw_file.close()
except Exception as e:
    print(e)
```

Finding Partition Tables

- For the Qualcomm modems, the partition tables start with special magic: 0xaa73ee55 or 0x9a1b7daa.



Partition Table Analysis

- The partition table called ‘MIBIB’
- The bootloader file type is ‘Android bootimg’
- The system partition is YAFFS
  - Redundancy partition for upgrading
  - Multilevel bootloader for secure boot

```
Partition Version: 3
Partition Number: 50
Partition       Start           Size            Start(int)      Size(int)
MIBIB           00000000        0000000a        0x0             0x140000
OEMINFO         0000000a        00000050        0x140000        0xa00000
SBL2            0000005a        0000000c        0xb40000        0x180000
SBL2BACKUP      00000066        0000000c        0xcc0000        0x180000
CONTROL         00000072        0000000c        0xe40000        0x180000
SECURITY        0000007e        0000000c        0xfc0000        0x180000
RPM             0000008a        0000000c        0x1140000       0x180000
RPMBACKUP       00000096        0000000c        0x12c0000       0x180000
EFS2            000000a2        00000058        0x1440000       0xb00000
EFS2_A          000000fa        00000058        0x1f40000       0xb00000
EFSBACKUP1      00000152        00000038        0x2a40000       0x700000
EFSBACKUP2      0000018a        00000038        0x3140000       0x700000
APPSBL          000001c2        0000000c        0x3840000       0x180000
APPSBL_A        000001ce        0000000c        0x39c0000       0x180000
APPS            000001da        00000040        0x3b40000       0x800000
APPS_A          0000021a        00000040        0x4340000       0x800000
MTCHUB          0000025a        00000018        0x4b40000       0x300000
USERDATA        00000272        00000030        0x4e40000       0x600000
```

Removing Spare Area

- The same as Hisilicon cellular module NAND flash.
- The bootloader prints the partition layout when power on.
- The HISI development kit (DVK) partitions are the same as the HERMES.

```
[0000008ms]NO. |offset |loadsize |capacity |loadaddr |entry |property |count |id |name |
[0000009ms]-------------------------------------------
[000000Ams]00000001: 00000000 ,00000000 ,00040000 ,00000000 ,00000000 ,00004000 ,00000000 ,00000101 ,m3boot
[000000Ams]00000002: 00040000 ,00000000 ,001c0000 ,4fe00000 ,4fe00000 ,00004000 ,00000000 ,00000102 ,fastboot
[000000Bms]00000003: 00200000 ,00000000 ,00200000 ,00000000 ,00000000 ,00004800 ,00000000 ,00000103 ,nvbacklte
[000000Cms]00000004: 00400000 ,00000000 ,00400000 ,00000000 ,00000000 ,00004000 ,00000000 ,00000104 ,nvimg
[000000Cms]00000005: 00800000 ,00000000 ,00400000 ,00000000 ,00000000 ,00004000 ,00000000 ,00000105 ,nvdload
[000000Dms]00000006: 00c00000 ,00000000 ,00200000 ,00000000 ,00000000 ,00004000 ,00000000 ,00000106 ,nvdefault
[000000Ems]00000007: 00e00000 ,00000000 ,00400000 ,00000000 ,00000000 ,00004000 ,00000000 ,0000010d ,oeminfo
[000000Ems]00000008: 01200000 ,00000000 ,0be00000 ,00000000 ,00000000 ,00004001 ,00000000 ,00000116 ,online
[000000Fms]00000009: 0d000000 ,00000000 ,00800000 ,4ffc0000 ,4ffc0000 ,00004000 ,00000000 ,00000107 ,kernel
[0000010ms]0000000a: 0d800000 ,00000000 ,00800000 ,4ffc0000 ,4ffc0000 ,00004000 ,00000000 ,00000108 ,kernelbk
[0000010ms]0000000b: 0e000000 ,00000000 ,00200000 ,00000000 ,00000000 ,00004000 ,00000000 ,00000109 ,m3image
[0000011ms]0000000c: 0e200000 ,00000000 ,00600000 ,00000000 ,00000000 ,00004000 ,00000000 ,0000010b ,dsp
```

DVK boot log

Removing Spare Area

- The partition table start with ‘pTableHead’ in the NAND dump.
- The structure is defined in /drivers/mtd/nand/ptable/ptable_def.h

```
/*----------------------- |  0 byte
 |"pTableHead"            |
 *----------------------- | 16 byte (partition head flag string)
 | the property of table  |
 *----------------------- | 20 byte (partition head flag string)
 |"V7R2_FPGA" (example.)  |
 *----------------------- | 48 byte (partition table version name)
 | <partition info>       |
 |  (size 32byte)         |
 *----------------------- | 96 byte
 | < partition info >     |
 |  (size 32byte)         |
 |------------------------| 144 byte
 :    ............        :
 :    ............        :
 |------------------------| 48 x N byte
 | < partition info >     |
 |  (size 32byte)         |
 |------------------------| 48 x (N+1) byte
 | "T"  (table end flag)  |
 |                        |
 |------------------------| */
```

pTableHead Structure

Partition Table Analysis

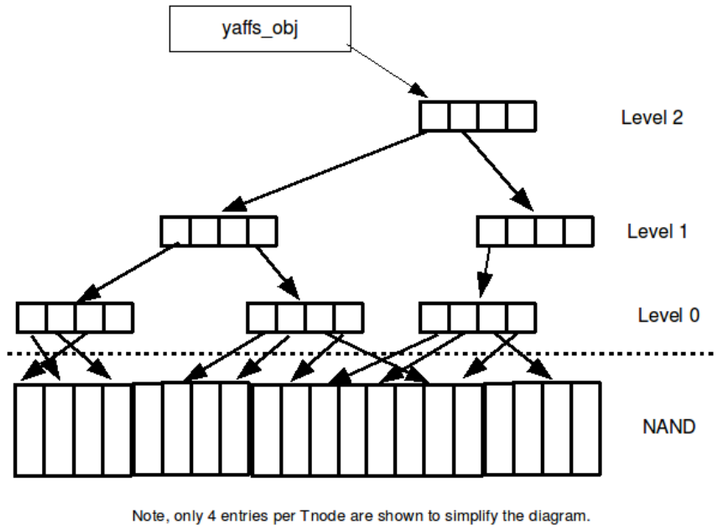
- We can parse the partition table with python. ☺
  - Balong V7R22 Telematic
  - It's similar with V7R22 4G Router (4PDA.ru)
  - Redundant Partitions
  - The key partition is YAFFS, too.

HISI Dumper: ptable 1.00 V7R22_TELEMATIC

name	offset	size	loadaddr	type	property
m3boot	0×00000000	0×00040000	0×00000000	IMAGE_M3BOOT	MTD
fastboot	0×00040000	0×00100000	0×afcff000	IMAGE_FASTBOOT	MTD
fastbootbk	0×00140000	0×00100000	0×afcff000	IMAGE_FASTBOOTBK	MTD
oeminfo	0×00240000	0×00200000	0×00000000	IMAGE_OEMINFO	MTD
nvbacklte	0×00440000	0×00500000	0×00000000	IMAGE_NVBACKLTE	Protected,MTD
nvbackltebk	0×00940000	0×00500000	0×00000000	IMAGE_NVBACKLTEBK	Protected,MTD
nvdefault	0×00e40000	0×00200000	0×00000000	IMAGE_NVFACTORY	MTD
nvimg	0×01040000	0×00700000	0×00000000	IMAGE_NVIMG	MTD
nvsys	0×01740000	0×00500000	0×00000000	IMAGE_NVDLD	MTD
nvdload	0×01c40000	0×00400000	0×00000000	IMAGE_NVDLD	MTD
control	0×02040000	0×00180000	0×00000000	IMAGE_CONTROL	MTD
security	0×021c0000	0×00180000	0×00000000	IMAGE_SECURITY	MTD
m3image	0×02340000	0×00180000	0×00000000	IMAGE_M3IMAGE	MTD
m3imagebk	0×024c0000	0×00180000	0×00000000	IMAGE_M3IMAGEBK	MTD
teeos	0×02640000	0×00400000	0×00000000	IMAGE_TEEOS	MTD
teeosbk	0×02a40000	0×00400000	0×00000000	IMAGE_TEEOSBK	MTD
dts	0×02e40000	0×00200000	0×00000000	IMAGE_DTS	MTD
dtsbk	0×03040000	0×00200000	0×00000000	IMAGE_DTSBK	MTD
hifi	0×03240000	0×00300000	0×00000000	IMAGE_HIFI	MTD
modem_fw	0×03540000	0×01e00000	0×00000000	IMAGE_MODEM_FW	YAFFS,MTD
boot	0×05340000	0×01000000	0×afdff000	IMAGE_KERNER	MTD
bootbk	0×06340000	0×01000000	0×afdff000	IMAGE_KERNELBK	MTD
nvimgbk	0×07340000	0×00700000	0×00000000	IMAGE_NVIMGBK	MTD
nvsysbk	0×07a40000	0×00500000	0×00000000	IMAGE_NVDLDBK	MTD
nvdloadbk	0×07f40000	0×00400000	0×00000000	IMAGE_NVDLDBK	MTD
hifibk	0×08340000	0×00300000	0×00000000	IMAGE_HIFIBK	MTD
modem_fwbk	0×08640000	0×01e00000	0×00000000	IMAGE_MODEM_FWBK	YAFFS,MTD
system	0×0a440000	0×02f80000	0×00000000	IMAGE_SYSTEM	YAFFS,MTD

Remapping YAFFS Logical Block

- The file system of user zone and system zone is YAFFS.
- Because of the Wear-Leveling, the block is not sequential. The block mapping info is in the OOB area. So we can’t mount the file-system directly. We made a tool to operate the file system.



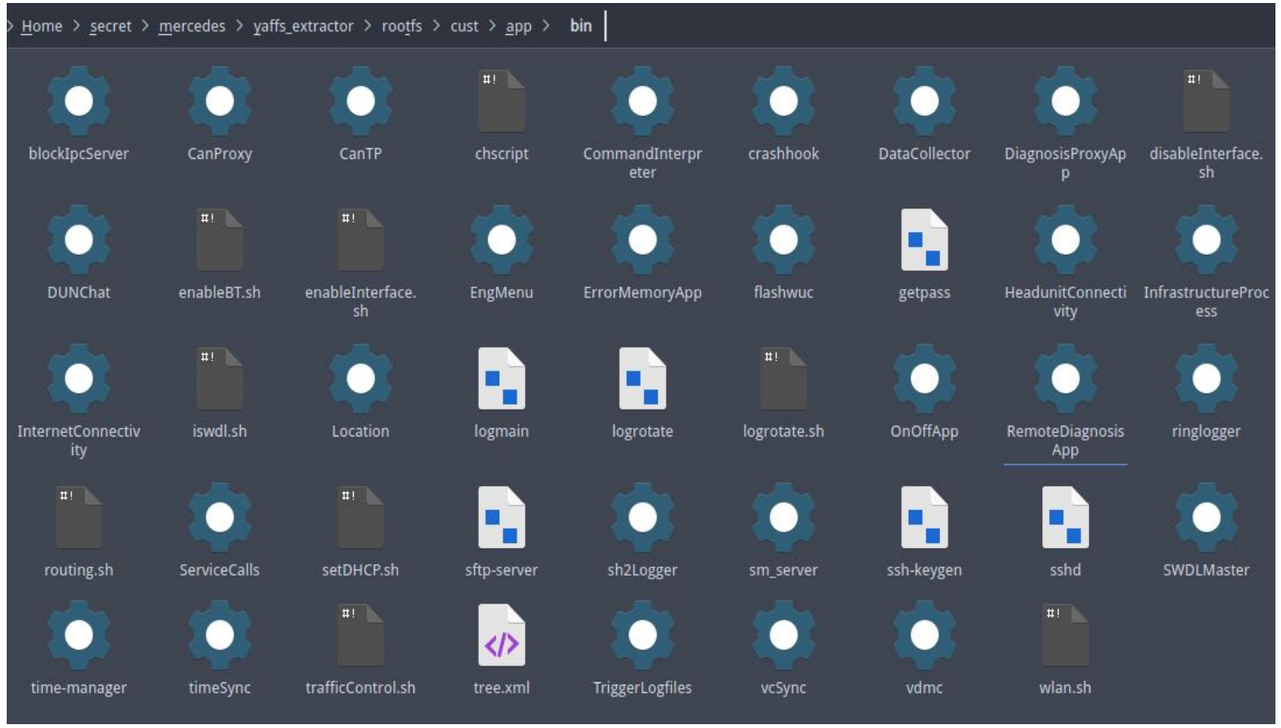
Note, only 4 entries per Tnode are shown to simplify the diagram.

```
if obj_type == YAFFS_OBJECT_TYPE_DIRECTORY:
    file_path = root_path + get_path(obj_id_list, parent_id, file_name)
    if not os.path.exists(file_path):
        try:
            os.makedirs(file_path)
        except:
            pass
elif obj_type == YAFFS_OBJECT_TYPE_FILE:
    file_path = root_path + get_path(obj_id_list, parent_id, file_name)
    print(file_path, largest_index)
    if not os.path.exists(file_path):
        try:
            obj.writeVersion(largest_index, file_path)
        except:
            pass
```

Extract files from YAFFS partition

Filesystem Extraction

- We extracted files from NAND flash.
- The OEM apps located at /cust/app/bin

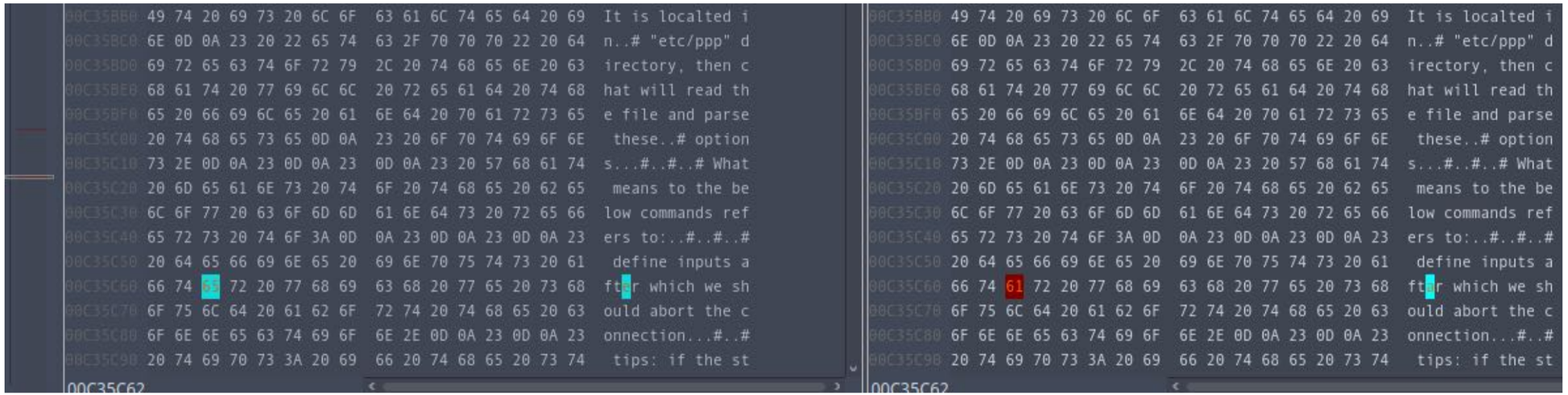


Bit-Flipping Error

- The bit-flipping is a NAND Flash features. If the key jump instructions are affected by bit-flipping, our research may have headed in a wrong direction.

After → Aftar

0b1100101 → 0b1100001



Error Bit Correction

- To fixed the bit flipping, we need to correct the bits by ECC.
- Different NAND has different ECC algorithm

```
#ifdef NANDC_SUPPORT_24BIT_ECC
    {NANDC_SIZE_8K,     368,    nandc6_ecc_24p1kbit,    &nandc6_oob32_layout  },

    {NANDC_SIZE_4K,     200,    nandc6_ecc_24p1kbit,    &nandc6_oob32_layout  },
#endif
    {NANDC_SIZE_4K,     144,    nandc6_ecc_8bit,        &nandc6_oob32_layout  },
    {NANDC_SIZE_4K,     88,     nandc6_ecc_4smb,        &nandc6_oob32_layout  },
#ifdef NANDC_SUPPORT_24BIT_ECC
    {NANDC_SIZE_2K,     116,    nandc6_ecc_24p1kbit,    &nandc6_oob32_layout  },
#endif
    {NANDC_SIZE_2K,     88,     nandc6_ecc_8bit,        &nandc6_oob32_layout  },
    {NANDC_SIZE_2K,     60,     nandc6_ecc_4smb,        &nandc6_oob32_layout  },
```

ECC definition in driver code

Generating ECC

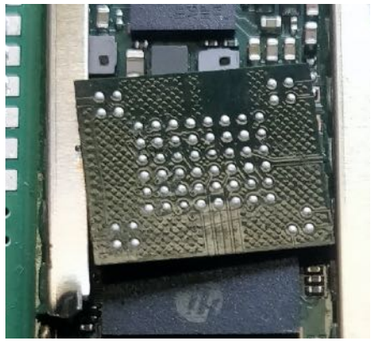
- The NAND controller using the hardware ECC, so the Linux driver source code dosen’t include ECC implementation.
- The SoC SDK including the ECC algorithm.
- 2k + 64-bytes: ecc_4bit

```
int ecc_parity_gen(byte[] data, int bits, int ecc_level, byte[] ecc_code) {
    switch(ecc_level) {
    case 8:
        this.lfsr_init( len: 112, "b1111111001111011100101111111111100101001110
        break;
```

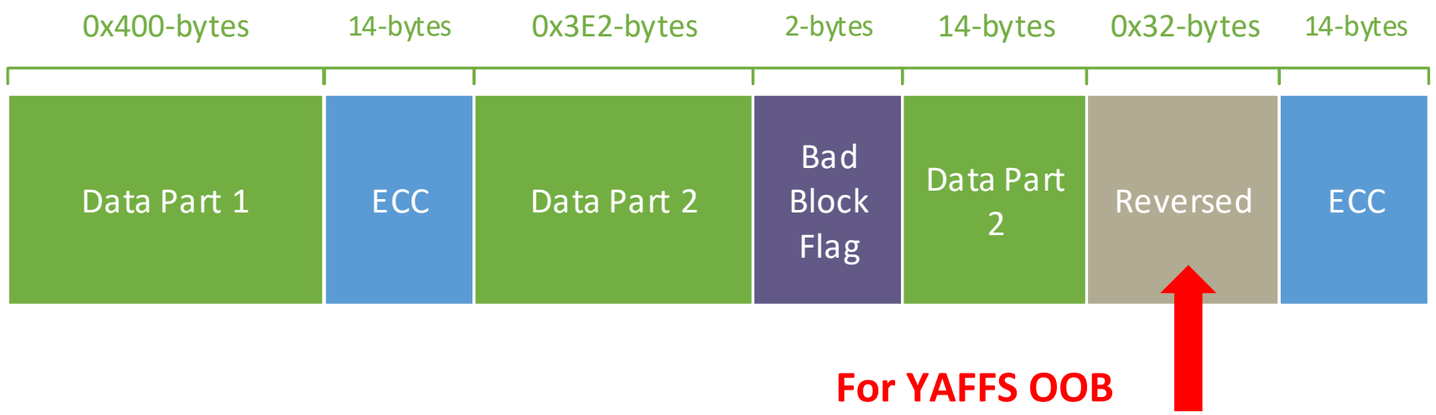
ECC Polynomial codewords

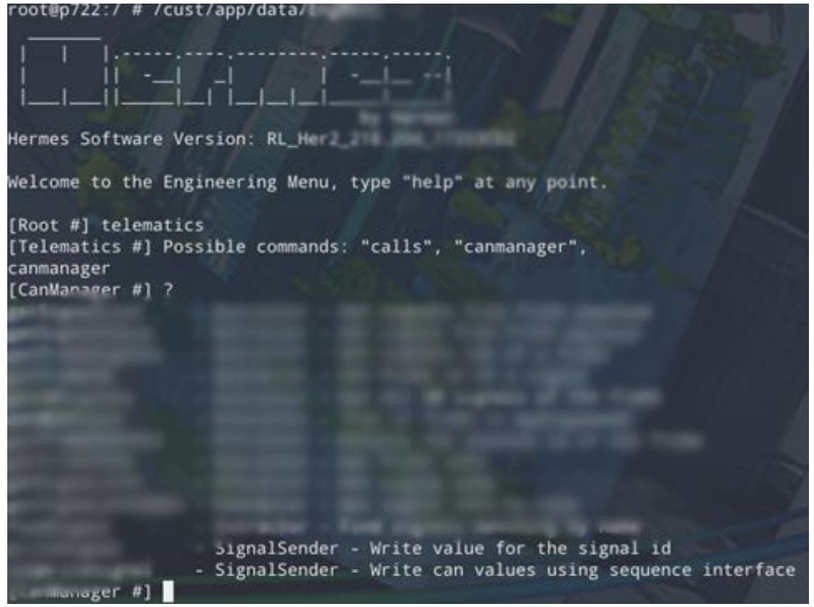
Final Works

- The NAND file we generated is the same as we dumped.
- No secure boot. We can
  - put a backdoor in it.
  - modify the system service to open a debug shell.



Reballing





Future Works

- Access the HERMES remotely.
  - For debugging purpose
- EngineerMode application.
  - Send CAN message with internal service
  - The data handled by SH-2A MCU
- Patch the MCU Firmware. (Difficulty: Nightmare)
  - Firmware analyzation.
  - Functional Verification.
  - It’s hard to buy a Renesas DVK
  - The chipset is the SH-2A

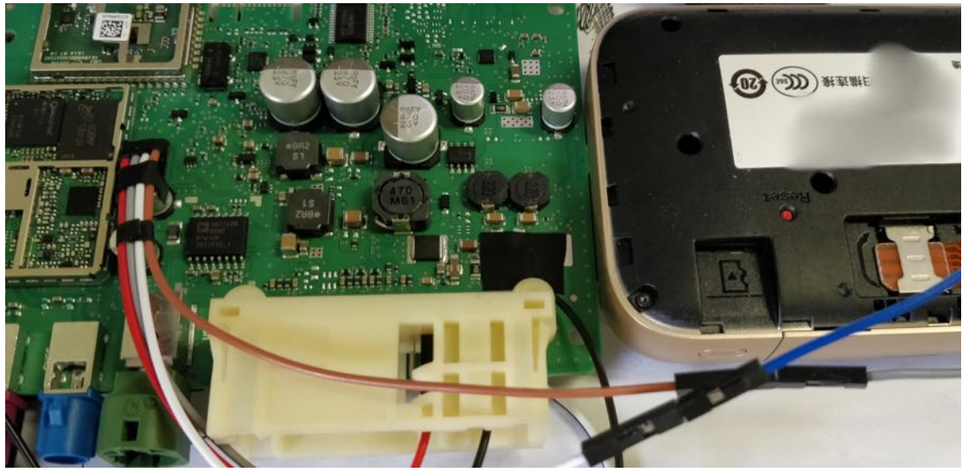
MCU log

Access Back-end via eSIM

- We configured the APN, wiring up the eSIM to SIM Extender.
- DON’T insert it to your 4G device right away.

```
[redacted].CLFU.NJM2MAPN
[redacted].CLFU.NJM2MAPN
[redacted].CLFU.NJM2MAPN usernameABCXYZ3
[redacted].CLFU.NJM2MAPN usernameABCXYZ4
DefaultValue1 usernameABCXYZ1
DefaultValue2 usernameABCXYZ2
```

APN name



Wiring up to eSIM

Access Back-end via eSIM

- The trigger when detecting an IMEI change event, it will freeze the account.

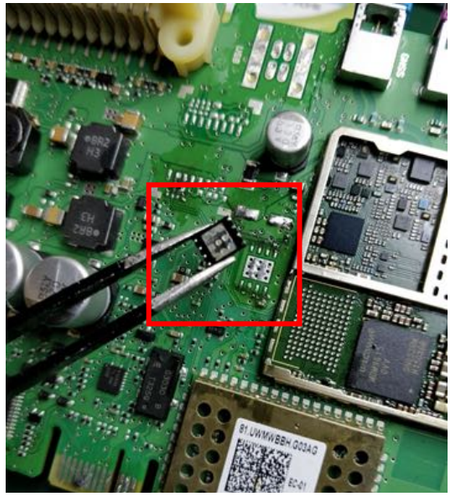
IMEI Change	Security	IMEI Change
IMEI Changed	Security	IMEI Change

- So we must change the IMEI as the same as the TCU, you need
  - 4G module DVK, it’s unlocked.
  - Modified 4G routers (E5885L).
  - An MTK mobile device.

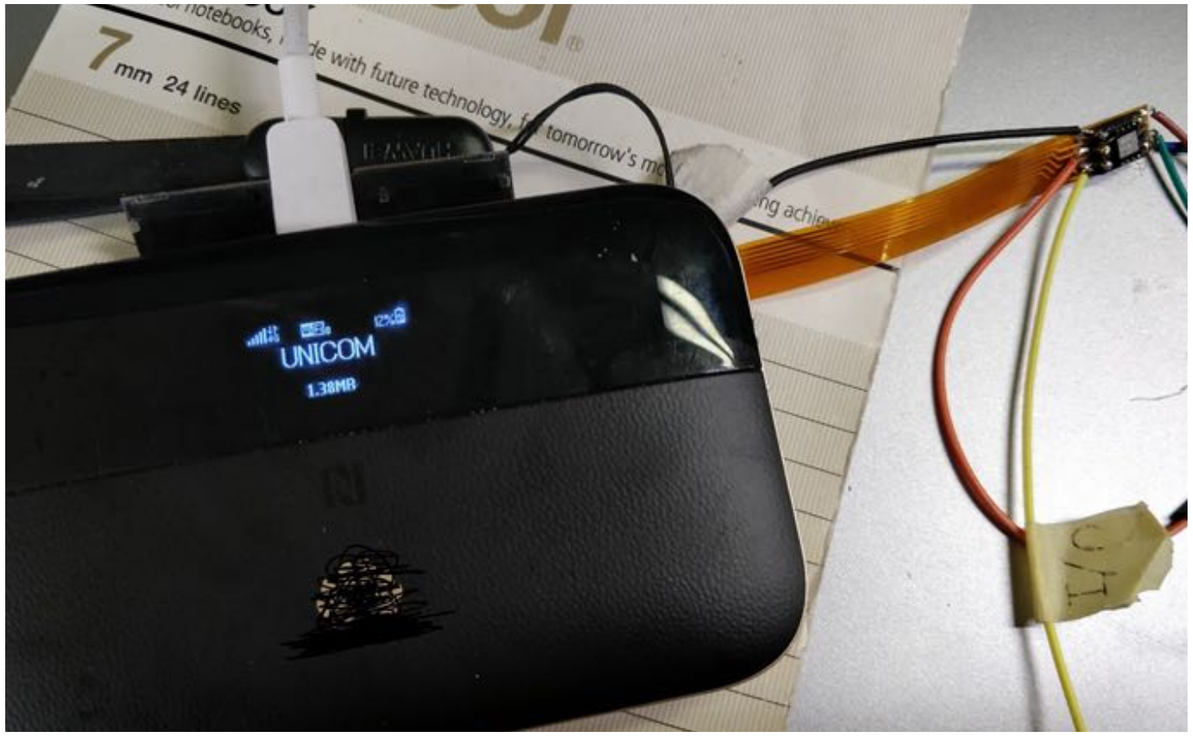
Hisilicon DVK

Access Back-end via eSIM

- We change the IMEI and used new eSIM from another HERMES



Teardown eSIM



Access Back-end via eSIM

- We got an intranet IPv4 address.
- The intranet is isolated.

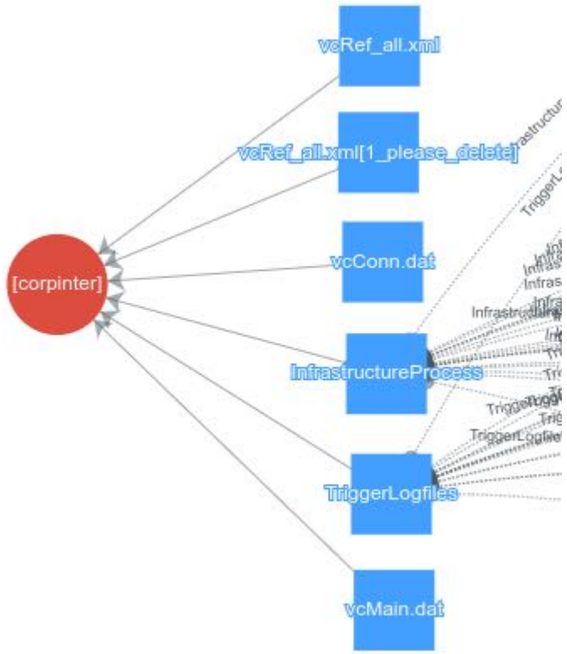
```
eth_x     Link encap:Ethernet  HWaddr 58:02:03:04:05:06
          inet addr:10.232.231.5  Bcast:10.255.255.255  Mask:255.2
          UP BROADCAST RUNNING MULTICAST  MTU:1500  Metric:1
          RX packets:288098 errors:0 dropped:0 overruns:0 frame:0
          TX packets:238666 errors:0 dropped:0 overruns:0 carrier:
          collisions:0 txqueuelen:1000
          RX bytes:322761113 (307.8 MiB)  TX bytes:24312369 (23.1
```

Access Back-end via eSIM

- The eSIM account is run out of credit, but it still can access to provider's mobile shop.
- It doesn't forbid us to access the TSP.

Finding domains

- The domain is corpinter, so we scan the domains from these files
- It's helpful for the penetration test.



vcRef_all.xml

Search Symbol in Graph

Path: /workspace/HERMES_1.5/ME919_NAND/online/vcRef_all.xml

Size: 42287 / 41 KB

MIME:text/xml / Charset: us-ascii

Keyword: corpinter

<CON_CepURL_CHN>https:/[redacted]mbiis_ce/index.htm</CON_CepURL_CHN>

<NTP_SERVER_URL>0.time.dvb.corpinter.net</NTP_SERVER_URL>

<NTP_SERVER_URL_BACKUP>1.time.dvb.corpinter.net</NTP_SERVER_URL_BACKUP>

MD5: 647f3aa98c3d3ebcb3a07cb1dd3df463

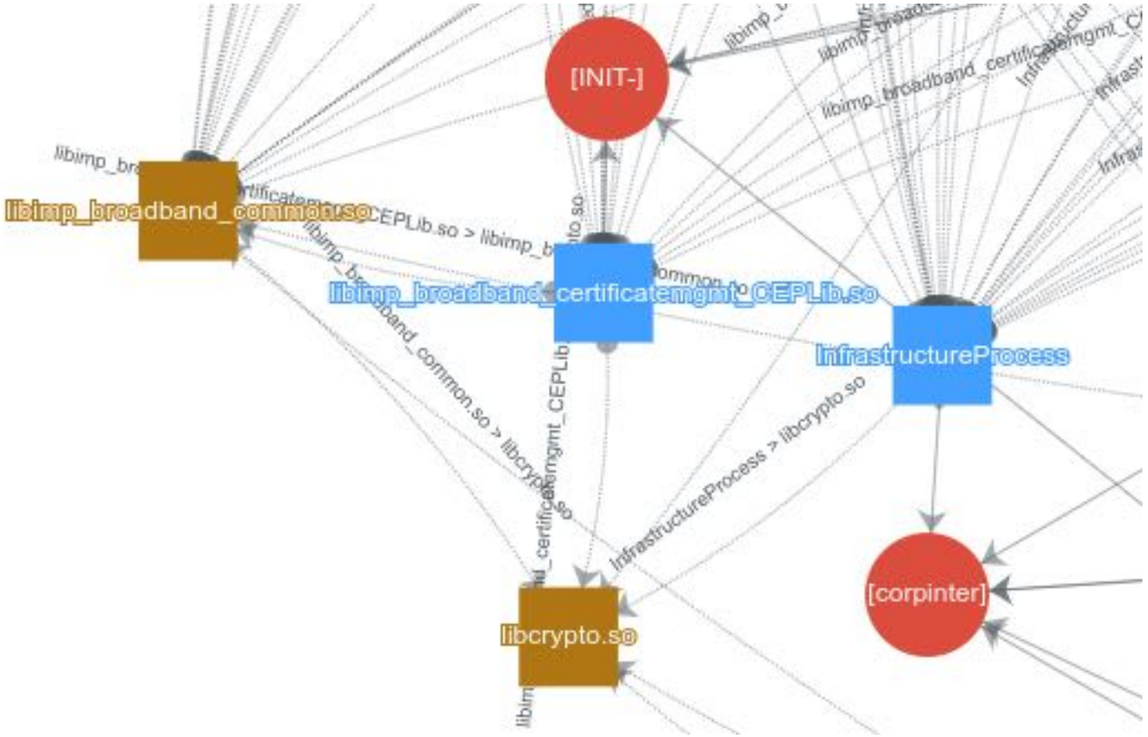
SHA1: 57af2e9d75233ffc739d71f22d5d55f54e7f3204

SHA256: c1cc5f32b52efa0fe792a6921415399acc2aa07b0ce3909022dcd5e18586f2f2

Associations between keyword and files

PFX Password Decryption

- The PFX file loaded by certificate management service
- InfrastructureProcess connects to the backend

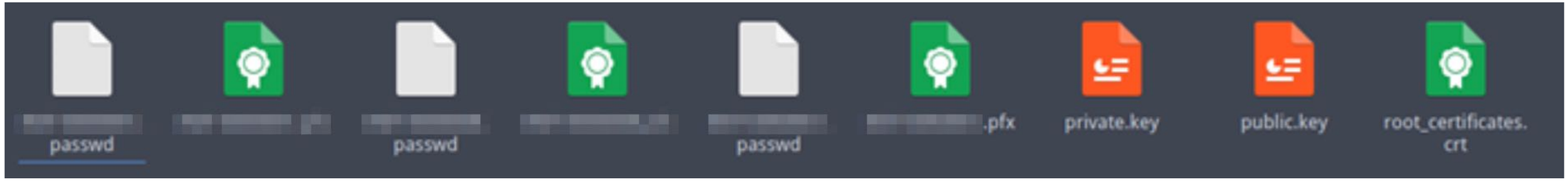


Analyzing file references in graph

Certificates

- The scanner reported that some public/private keys and certificates.

- But the scanner cannot decrypt the PFX file, we found these files manually including pkcs12 client certificates, encrypted passwords and CA certificates for the car backend server.



Certificates, passwords and keypairs

PFX Password Decryption

- HERMES client inits with PFX file and passwd file.
- There are three regions certificates.
  - INIT-006xxx1
  - INIT-00xxxx1
  - INIT-00xxxx8

```
std::string::string(&MBCA, "MB-CA", &v3);
_aeabi_atexit(&MBCA, &std::string::~string, &off_25000);
std::string::string(&MBIISCA, "MBIIS CA", &v3);
_aeabi_atexit(&MBIISCA, &std::string::~string, &off_25000);
std::string::string(&init60001pfx, "/cust/app/data/connectivity/INIT-[redacted].pfx", &v3);
_aeabi_atexit(&init60001pfx, &std::string::~string, &off_25000);
std::string::string(&init60001passwd, "/cust/app/data/connectivity/INIT-[redacted].passwd", &v3);
_aeabi_atexit(&init60001passwd, &std::string::~string, &off_25000);
std::string::string(&init00001pfx, "/cust/app/data/connectivity/INIT-[redacted].pfx", &v3);
_aeabi_atexit(&init00001pfx, &std::string::~string, &off_25000);
std::string::string(&init00001passwd, "/cust/app/data/connectivity/INIT-[redacted].passwd", &v3);
_aeabi_atexit(&init00001passwd, &std::string::~string, &off_25000);
std::string::string(&coneectivity_dir, "/var/connectivity", &v3);
_aeabi_atexit(&coneectivity_dir, &std::string::~string, &off_25000);
std::string::string(&regular_pfx, "/cust/data/persistency/regular.pfx", &v3);
_aeabi_atexit(&regular_pfx, &std::string::~string, &off_25000);
std::string::string(&regular_tmp_pfx, "/cust/data/persistency/regular_tmp.pfx", &v3);
_aeabi_atexit(&regular_tmp_pfx, &std::string::~string, &off_25000);
std::string::string(&regular_passwd, "/cust/data/persistency/regular.passwd", &v3);
_aeabi_atexit(&regular_passwd, &std::string::~string, &off_25000);
std::string::string(&SK_SMS_0, "/cust/data/persistency/SK-SMS-0", &v3);
_aeabi_atexit(&SK_SMS_0, &std::string::~string, &off_25000);
std::string::string(&SK_SMS_1, "/cust/data/persistency/SK-SMS-1", &v3);
_aeabi_atexit(&SK_SMS_1, &std::string::~string, &off_25000);
std::string::string(&IV_SERIAL, "/cust/data/persistency/IV-SERIAL", &v3);
return _aeabi_atexit(&IV_SERIAL, &std::string::~string, &off_25000);
```

Persistency files

PFX Password Decryption

- /****/***/lib/libimp_broadband_common.so provides crypto implement.
- AES256 Key is hardcoded.

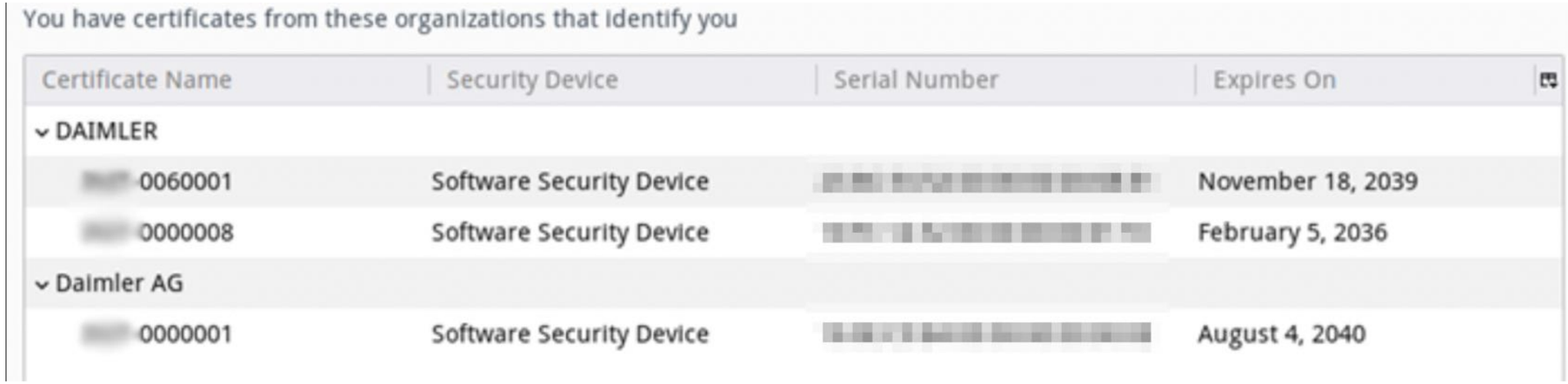
```
CEncryptionInterface::readEncrypted((int)&plain_text_passwd, (int)&xx);
std::string::operator=((int)xxflag, (int)&plain_text_passwd);
std::string::~string((std::string *)&plain_text_passwd);
std::string::~string((std::string *)&xx);
if ( std::string::compare(xxflag, (const char *)&unk_1F037) )
{
  v7 = (std::string *)(v2 + 20);
  if ( checkFile(regular_passwd, 0, 1) )
  {
    std::string::string((std::string *)&xx, (const std::string *)&regular_passwd);
    CEncryptionInterface::readEncrypted((int)&plain_text_passwd, (int)&xx);
    std::string::operator=((int)v7, (int)&plain_text_passwd);
    std::string::~string((std::string *)&plain_text_passwd);
    std::string::~string((std::string *)&xx);
```

```
str_cpy(&iv, (int)"[redacted]", (int)"");
AES_set_decrypt_key((int)"[redacted]", 256, (int)&key);
AES_cbc_encrypt(*in, (int)out, in[1] - *in, (int)&key, iv, 0);
```

IV and AES key

PFX Password Decryption

- We can load these certificates into browser, they didn't expire.
- The certificate name 0060001 is used for the China market.

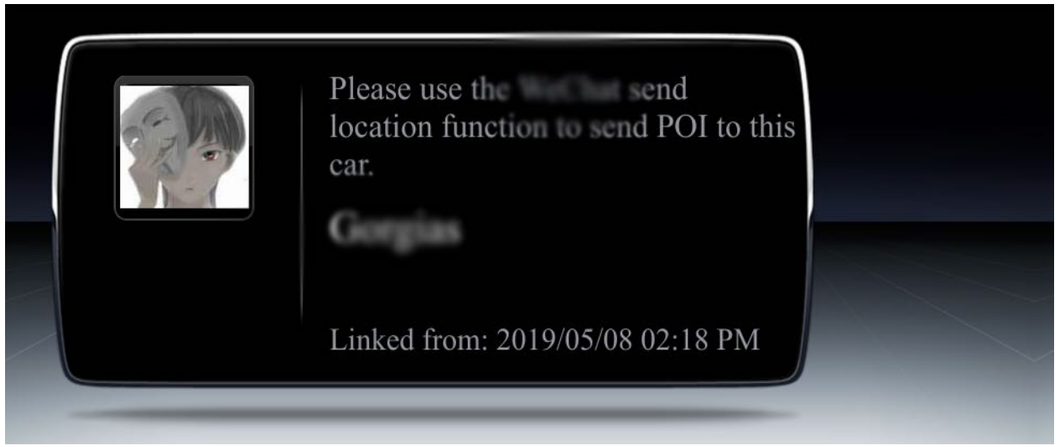

You have certificates from these organizations that identify you

Certificate Name	Security Device	Serial Number	Expires On
DAIMLER			
0060001	Software Security Device	[redacted]	November 18, 2039
0000008	Software Security Device	[redacted]	February 5, 2036
Daimler AG			
0000001	Software Security Device	[redacted]	August 4, 2040

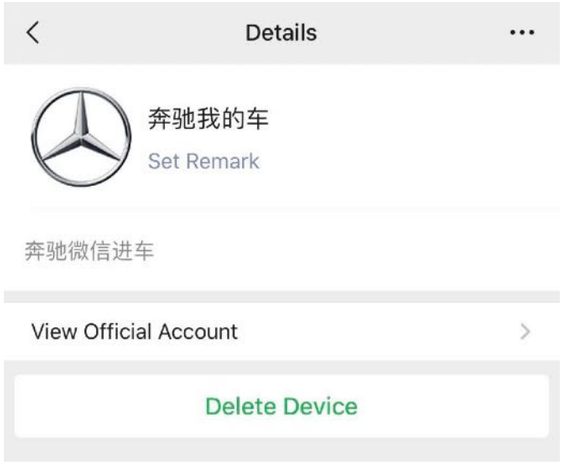
Available client certificates

Social Plugin SSRF

- You can bind your social media account with VIN in Head-Unit.
- The avatar URL is return to user from social media backend.
- We can modify the URL and submit it to TSP backend.



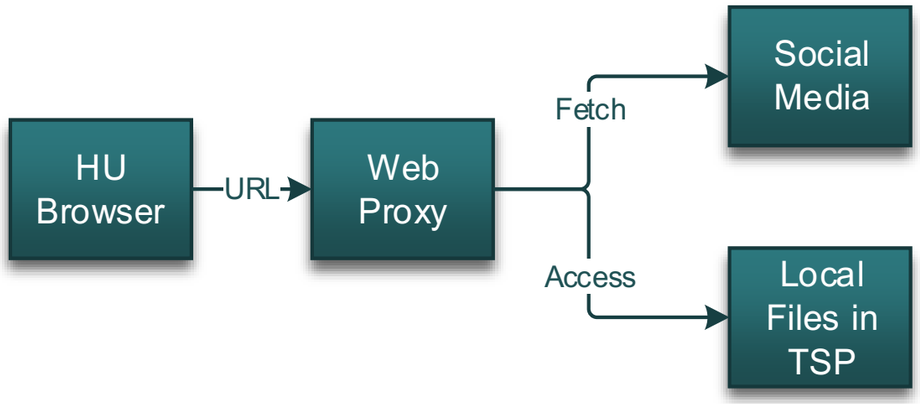
HeadUnit plugin page



The car is bind with my account

Social Plugin SSRF

The plugin service will load any URL we want to access.

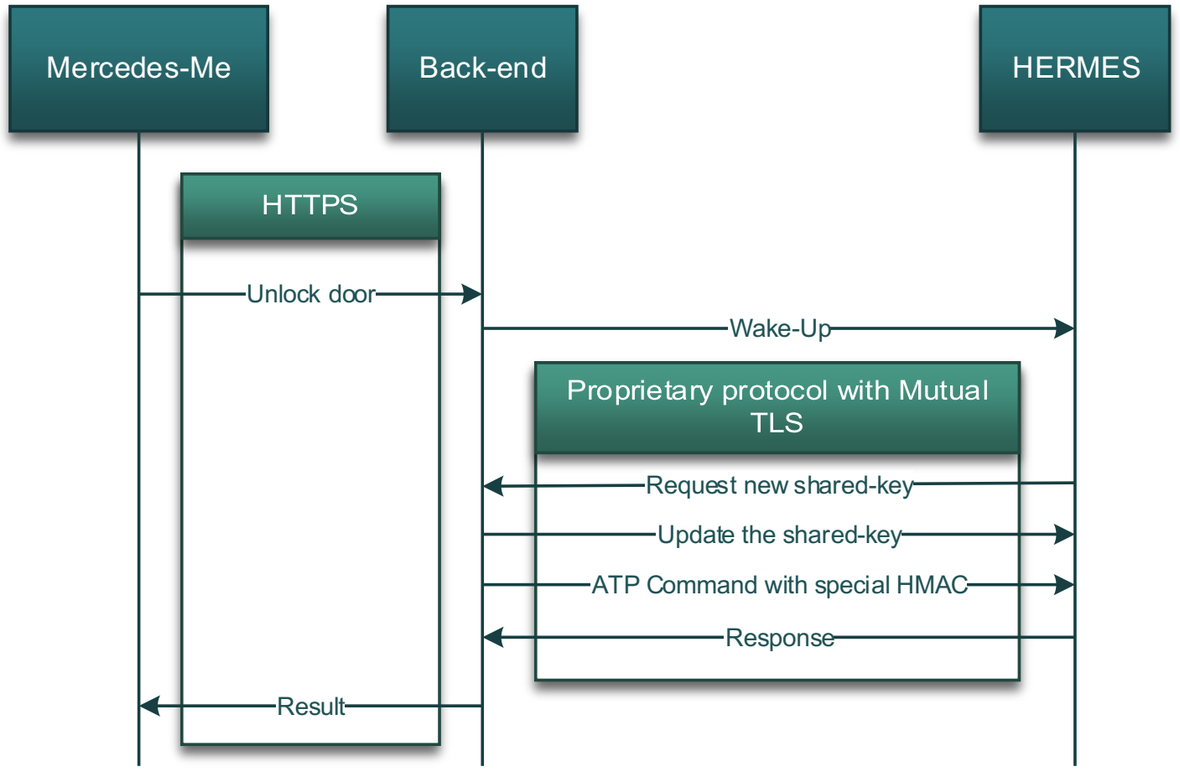


System file leaks

Telematics Data Stream

ATP: Advanced Telematics Protocol

- Support SMS channel, TCP channel
- Mutual TLS (TCP)
- Support Encryption
- Unique key-pairs
- Dynamic key/IV

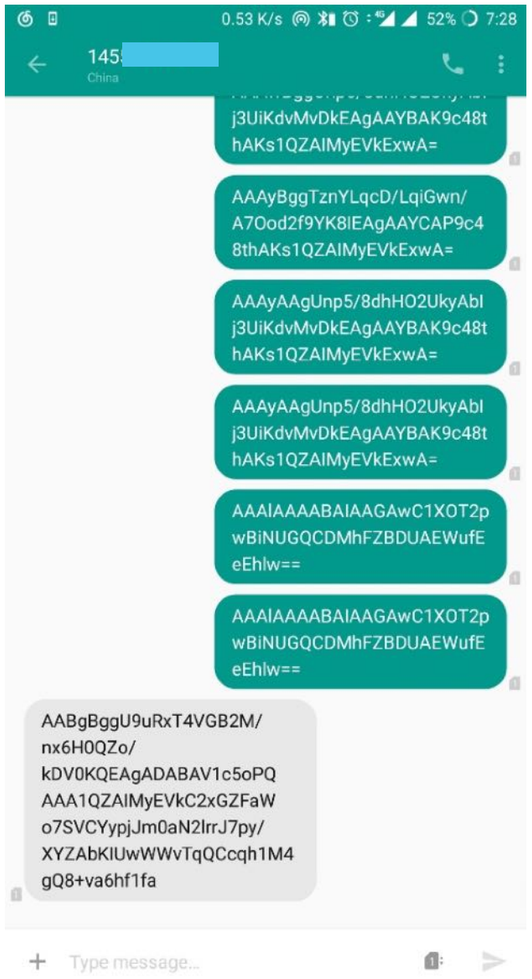


Car control data stream

SMS Communication

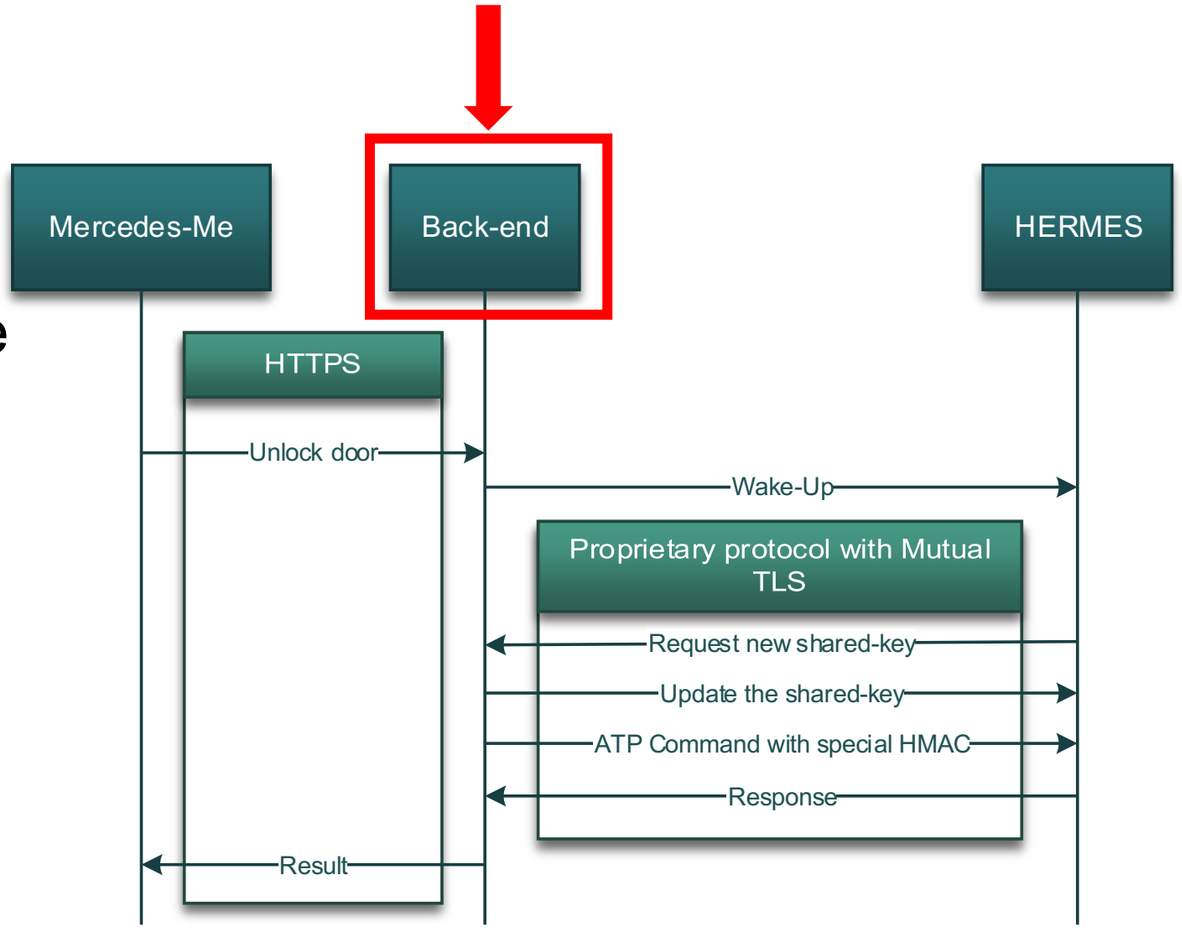
- Disconnect the TCU network, change the platform number to my phone number
- We can communicate with the TCU by using mobile phone.
- BUT it's secure. The algorithms are hmacSHA256 + AES256, we can't modify it or replay it.

```
char     msg_len[3]
char     security_flag
char     digest_algorithm
char     digest_len
char     digest_position[digest_len]
char     message_type[2] // 02 AES256CFB HMAC SHA256
1-byte   unknown
char     application_id[2] // 06 door 2b sigpos
19-bytes unknown
char     vin_length
```



Control Data Stream

- Car owners login Mercedes-Me from APP.
- The Back-end server didn't authenticate the requests from Mercedes-Me.
- Once we get the access to back-end, we can control any car in China.

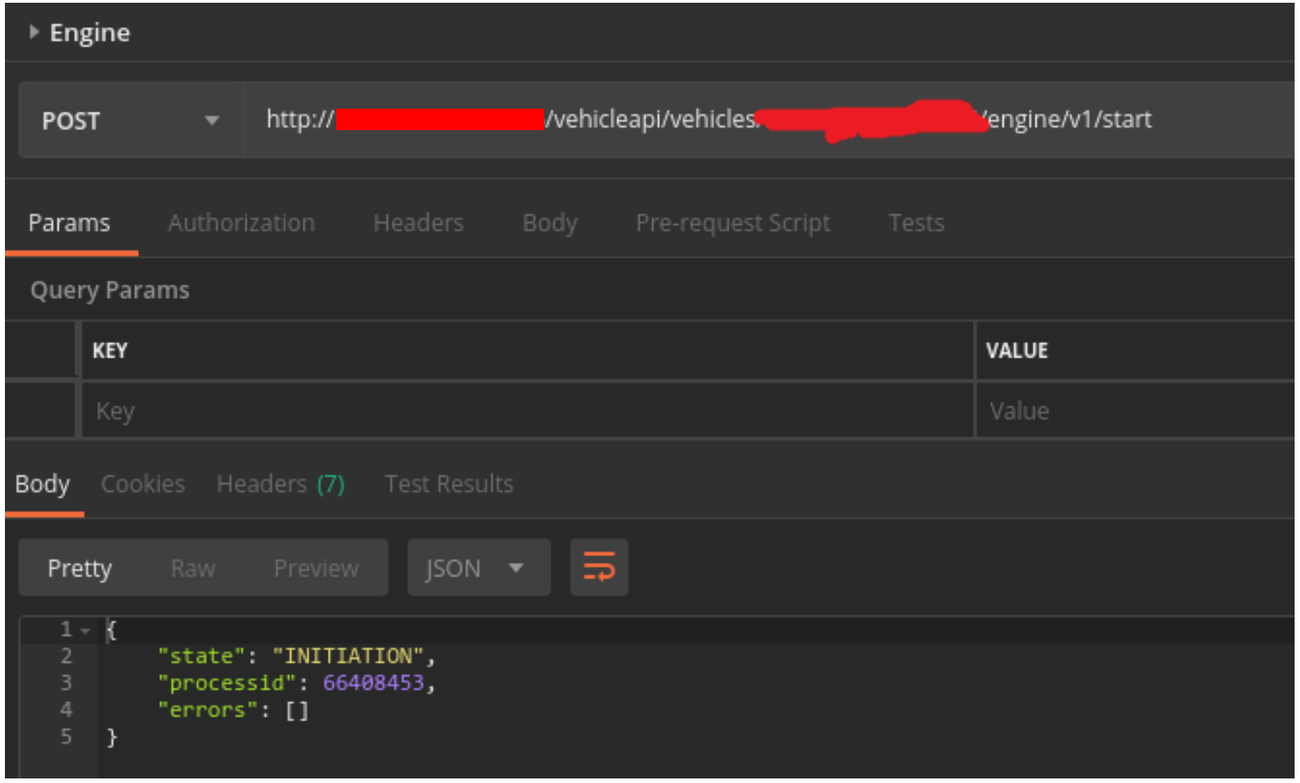


Car control data stream

Car Control Command

Supported Commands

- Door lock/unlock
- Roof open/close
- Lighting on/off
- Car beeping
- Engine start/stop (Limited)
  - Based on FBS4
  - Limited models
  - Value-added Service



Engine start success

Summary

- Follow Responsible Disclosure Policy
- Attack chain exploited hardware and software vulnerabilities
- Key impact: ability to send “remote services” commands (Didn’t go too far)
- We did see many security considerations in Mercedes-Benz Cars
- All access vulnerabilities were promptly fixed together

FleetSecOps in Action

Immediate Response Actions

- Step 1: Initiate & Analyze
  - Initiate incident response procedures
  - Mobilize investigation and response teams
  - Prioritize response activities
- Step 2: Contain & Fix
  - Selective blocking of services + immediate fixes
  - Forensic investigation
  - Long-term fixes development
- Step 3: Lessons Learned
  - Deploy long-term fix
  - Roll-out plan for hardening
  - Lessons learned exercise

Strong White Hat Community Is Key

BAD ACTORS


Thank You!